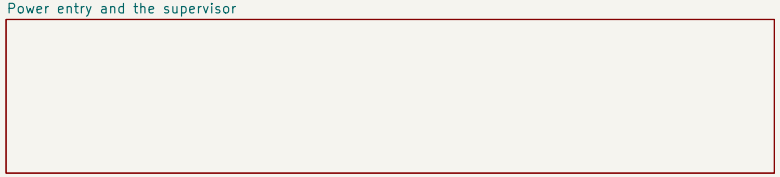
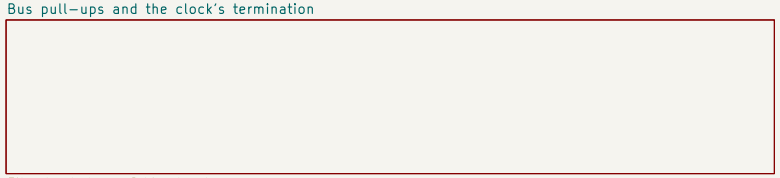
Power entry and the supervisor



File: backplane-1.kicad_sch

Bus pull-ups and the clock's termination



File: backplane-2.kicad_sch

Slots 0-1 (the debug card, the CPU)



File: backplane-3.kicad_sch

Slots 2-3



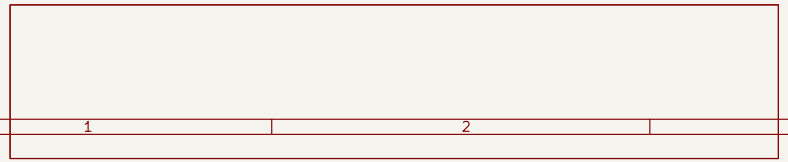
File: backplane-4.kicad_sch

Slots 4-5



File: backplane-5.kicad_sch

Slots 6-7



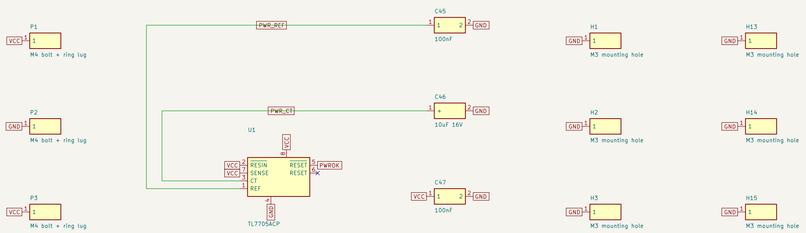
File: backplane-6.kicad_sch

Generated by generators/p32kicad_sch.py — do not edit
Root sheet: one block per functional sheet
P32W

Sheet: /
File: backplane.kicad_sch

Title: P32W BACKPLANE

Size: A4	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 1/9



Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit

Power entry and the supervisor

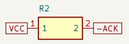
P32W

Sheet: /Power entry and the supervisor/

File: backplane-1.kicad_sch

Title: P32W BACKPLANE

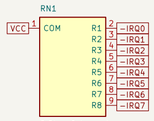
Size: A1	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 2/9

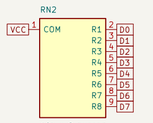


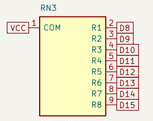


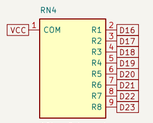


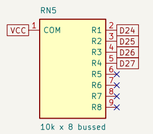


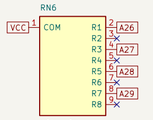


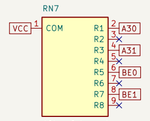


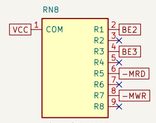


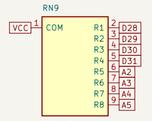


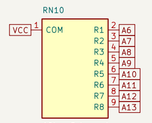


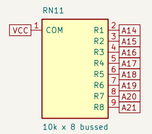


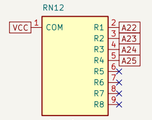


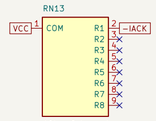
















Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit

Bus pull-ups and the clock's termination

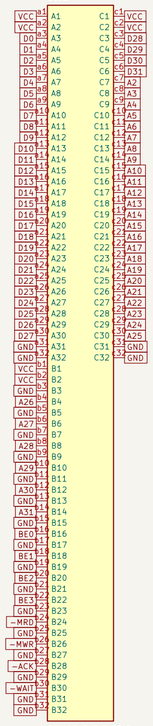
P32W

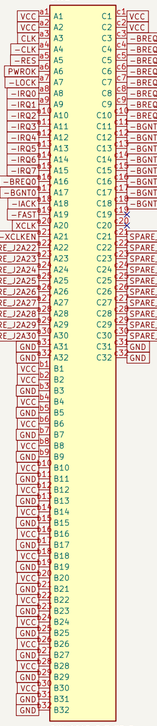
Sheet: /Bus pull-ups and the clock's termination/

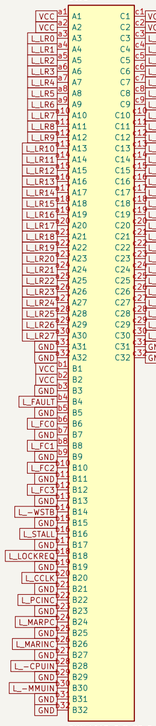
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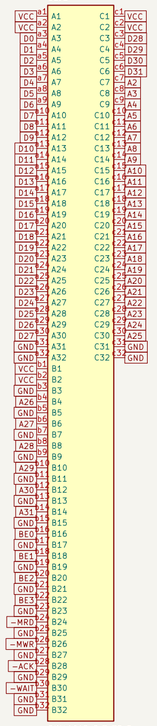
Title: P32W BACKPLANE

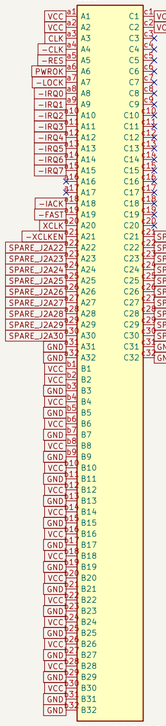
Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 3/9

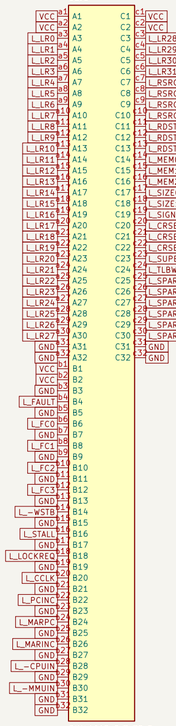












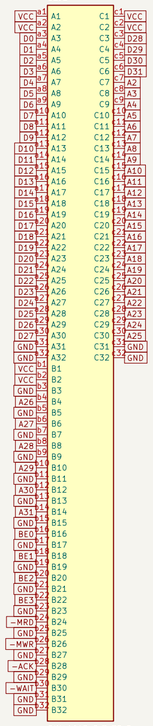
Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Slots 0–1 (the debug card, the CPU)

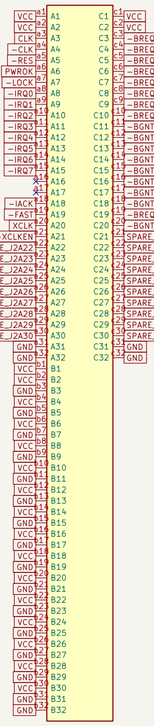
P32W

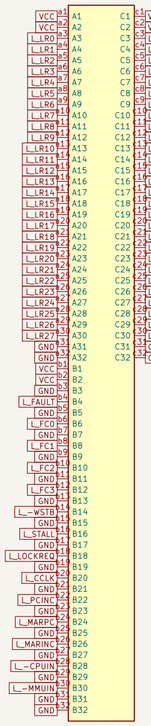
Sheet: /Slots 0–1 (the debug card, the CPU)/
File: backplane-3.kicad_sch

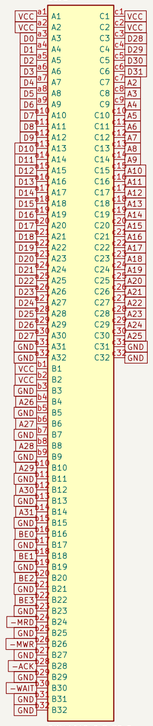
Title: P32W BACKPLANE

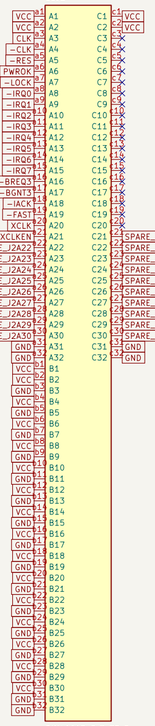
Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 4/9

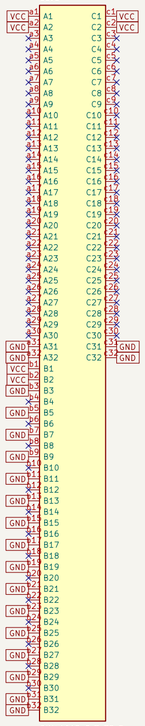










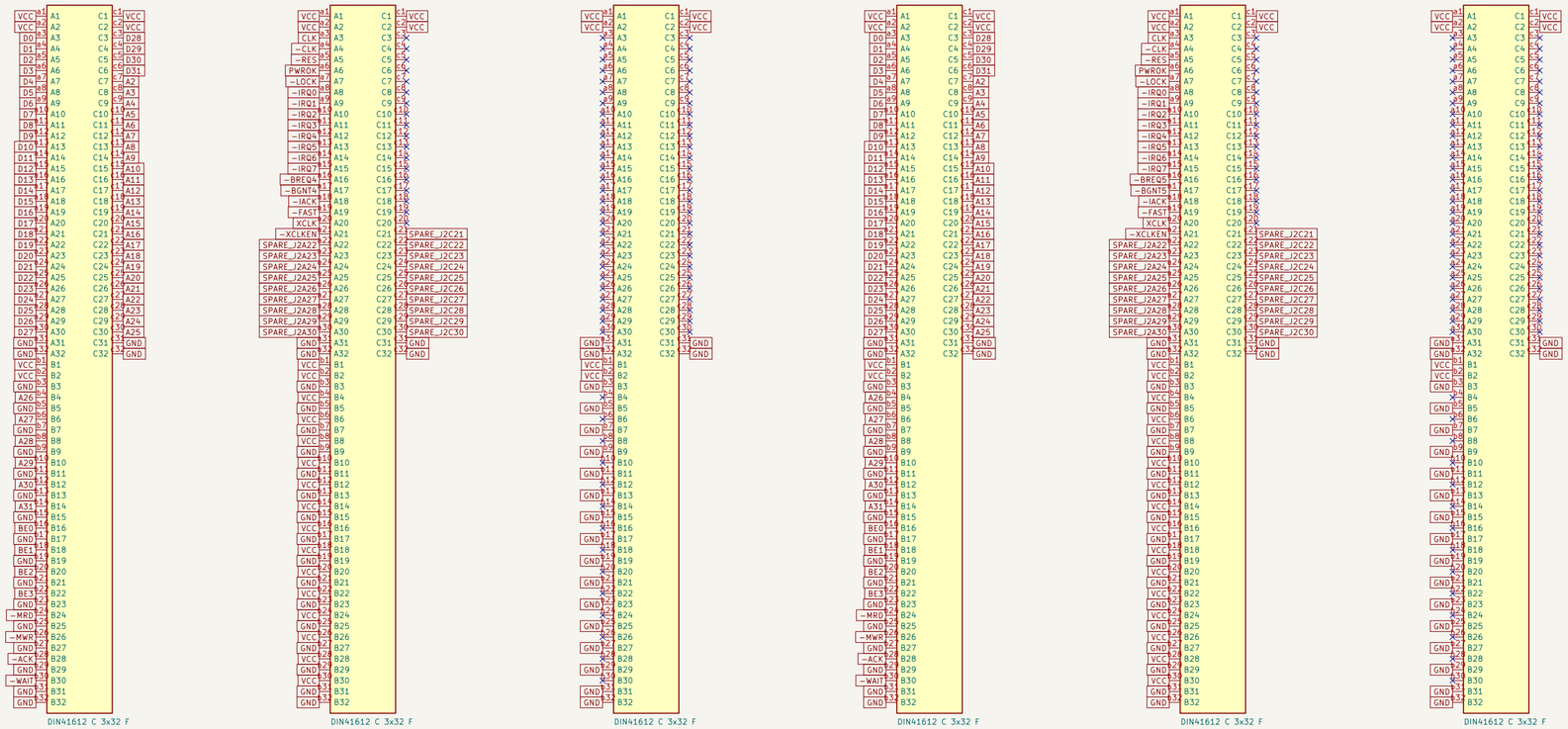


Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Slots 2–3
P32W

Sheet: /Slots 2–3/
File: backplane-4.kicad_sch

Title: P32W BACKPLANE

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 5/9



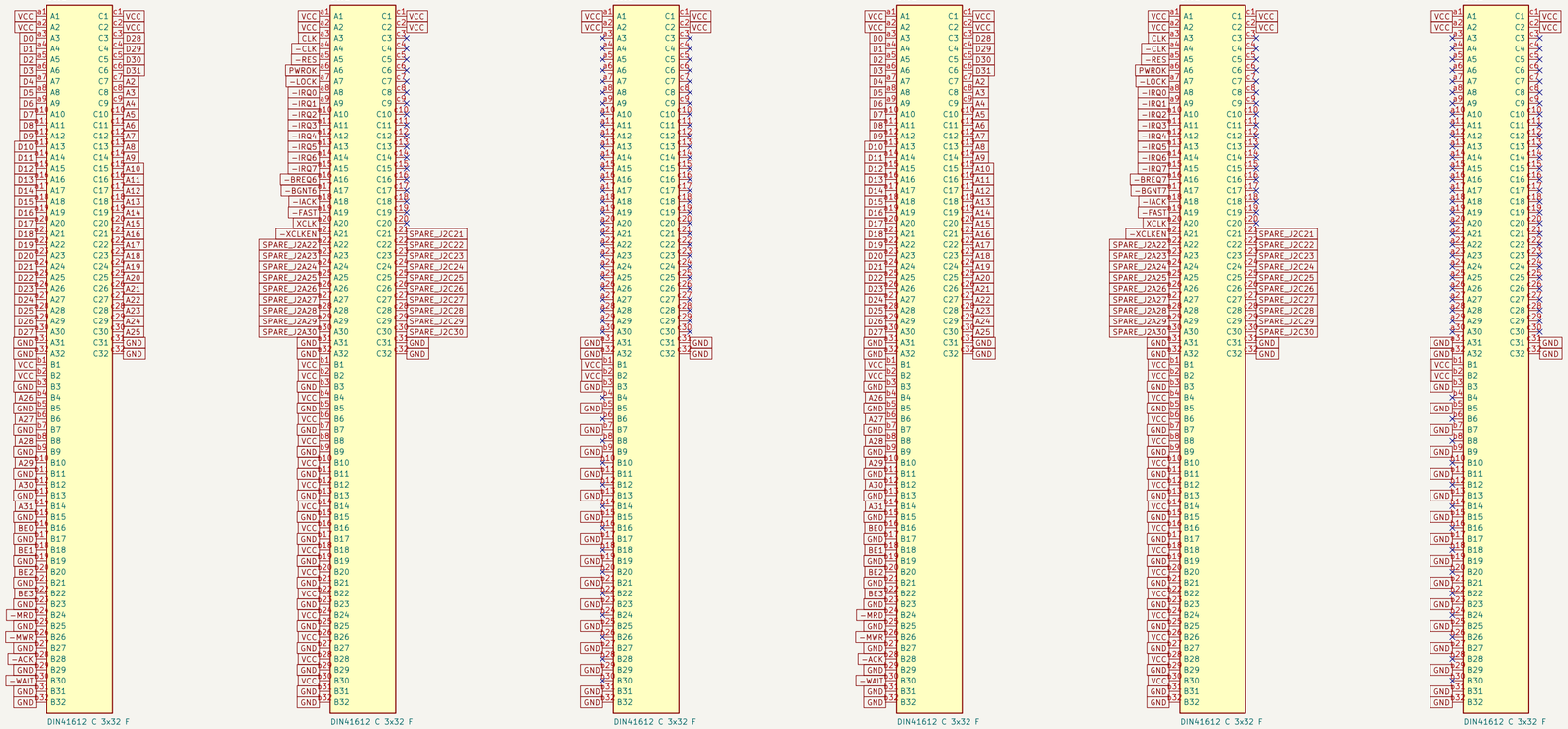
Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Slots 4–5

P32W

Sheet: /Slots 4–5/
File: backplane–5.kicad_sch

Title: P32W BACKPLANE

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 6/9

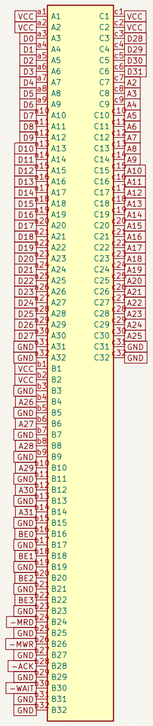


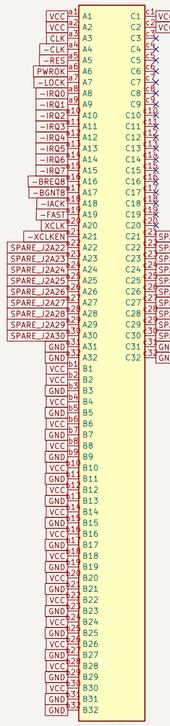
Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Slots 6–7
P32W

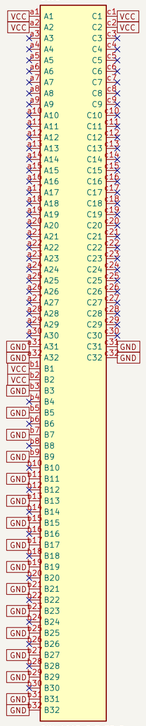
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File: backplane–6.kicad_sch

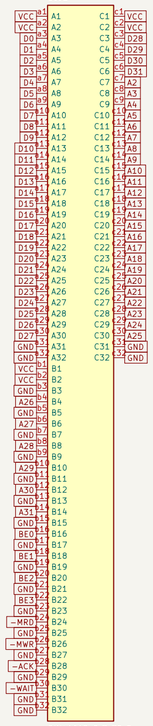
Title: P32W BACKPLANE

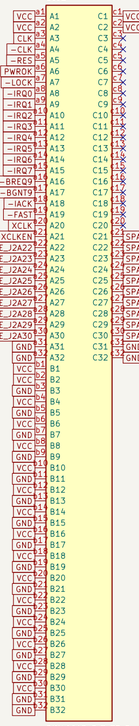
Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 7/9

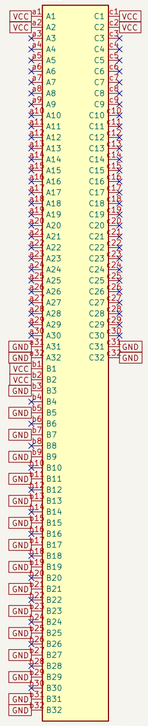












Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Slots 8–9
P32W

Sheet: /Slots 8–9/
File: backplane–7.kicad_sch

Title: P32W BACKPLANE

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 8/9

C3
VCC 1 1 2 2 GND
100nF

C15
VCC 1 1 2 2 GND
100nF

C27
VCC 1 1 2 2 GND
100nF

C39
VCC 1 1 2 2 GND
100nF

C4
VCC 1 1 2 2 GND
100nF

C16
VCC 1 1 2 2 GND
100nF

C28
VCC 1 1 2 2 GND
100nF

C40
VCC 1 1 2 2 GND
100nF

C5
VCC 1 1 2 2 GND
100nF

C17
VCC 1 1 2 2 GND
100nF

C29
VCC 1 1 2 2 GND
100nF

C41
VCC 1 1 2 2 GND
100nF

C6
VCC 1 + 2 GND
10uF 16V

C18
VCC 1 + 2 GND
10uF 16V

C30
VCC 1 + 2 GND
10uF 16V

C42
VCC 1 + 2 GND
10uF 16V

C7
VCC 1 1 2 2 GND
100nF

C19
VCC 1 1 2 2 GND
100nF

C31
VCC 1 1 2 2 GND
100nF

C8
VCC 1 1 2 2 GND
100nF

C20
VCC 1 1 2 2 GND
100nF

C32
VCC 1 1 2 2 GND
100nF

C9
VCC 1 1 2 2 GND
100nF

C21
VCC 1 1 2 2 GND
100nF

C33
VCC 1 1 2 2 GND
100nF

C10
VCC 1 + 2 GND
10uF 16V

C22
VCC 1 + 2 GND
10uF 16V

C34
VCC 1 + 2 GND
10uF 16V

C11
VCC 1 1 2 2 GND
100nF

C23
VCC 1 1 2 2 GND
100nF

C35
VCC 1 1 2 2 GND
100nF

C12
VCC 1 1 2 2 GND
100nF

C24
VCC 1 1 2 2 GND
100nF

C36
VCC 1 1 2 2 GND
100nF

C13
VCC 1 1 2 2 GND
100nF

C25
VCC 1 1 2 2 GND
100nF

C37
VCC 1 1 2 2 GND
100nF

C14
VCC 1 + 2 GND
10uF 16V

C26
VCC 1 + 2 GND
10uF 16V

C38
VCC 1 + 2 GND
10uF 16V

PWR_FLAG
VCC

PWR_FLAG
GND

Generated by generators/p32kicad_sch.py from generators/card_backplane.py — do not edit
Power: decoupling and bulk capacitors
P32W
Sheet: /Power: decoupling and bulk capacitors/
File: backplane-B.kicad_sch

Title: P32W BACKPLANE

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 9/9