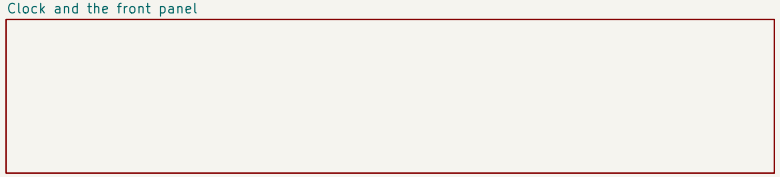
Clock and the front panel



File: cpu-card-1.kicad_sch

Control store and the pipeline register



File: cpu-card-2.kicad_sch

Sequencer



File: cpu-card-3.kicad_sch

Instruction register and decode



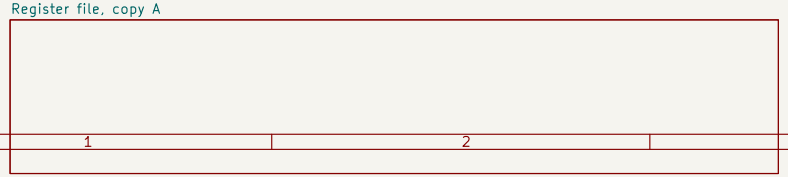
File: cpu-card-4.kicad_sch

Register addresses



File: cpu-card-5.kicad_sch

Register file, copy A



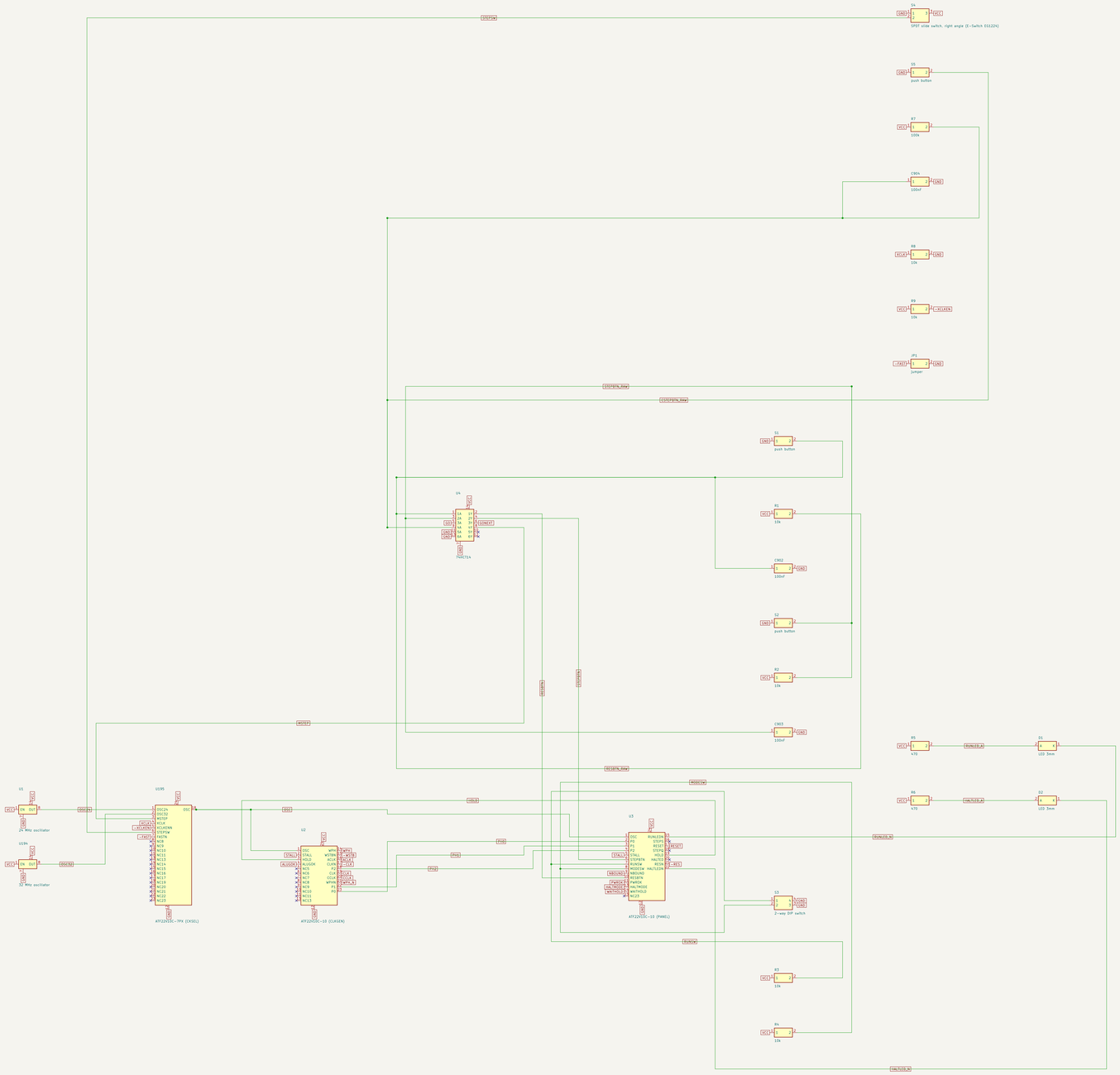
File: cpu-card-6.kicad_sch

Generated by generators/p32kicad_sch.py — do not edit
Root sheet: one block per functional sheet
P32W

Sheet: /
File: cpu-card.kicad_sch

Title: P32W CPU CARD

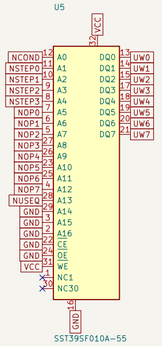
Size: A4	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 1/18

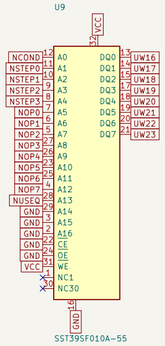


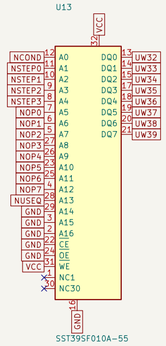
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Clock and the front panel
P32W
Sheet: /Clock and the front panel/
File: cpu-card-1.kicad_sch

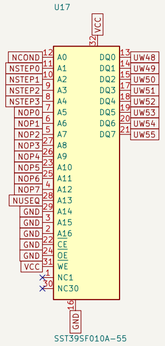
Title: P32W CPU CARD

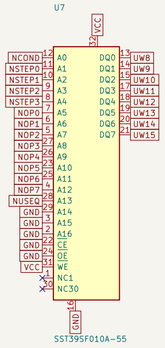
Size: A3	Date:	Rev:
KiCad E.D.A. 10.0.0		id: 2/18

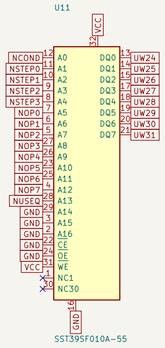


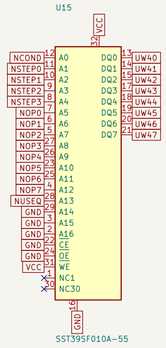


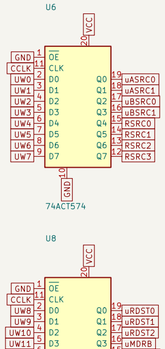


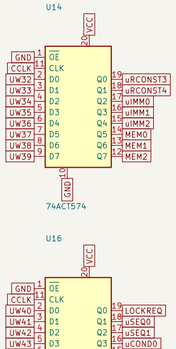


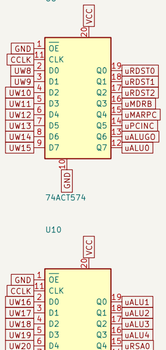


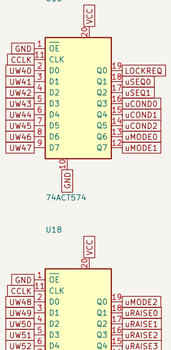


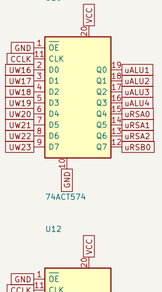


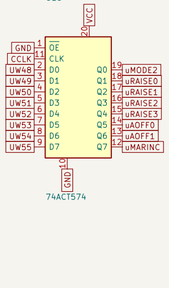


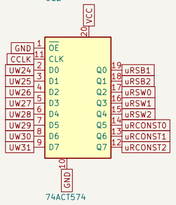












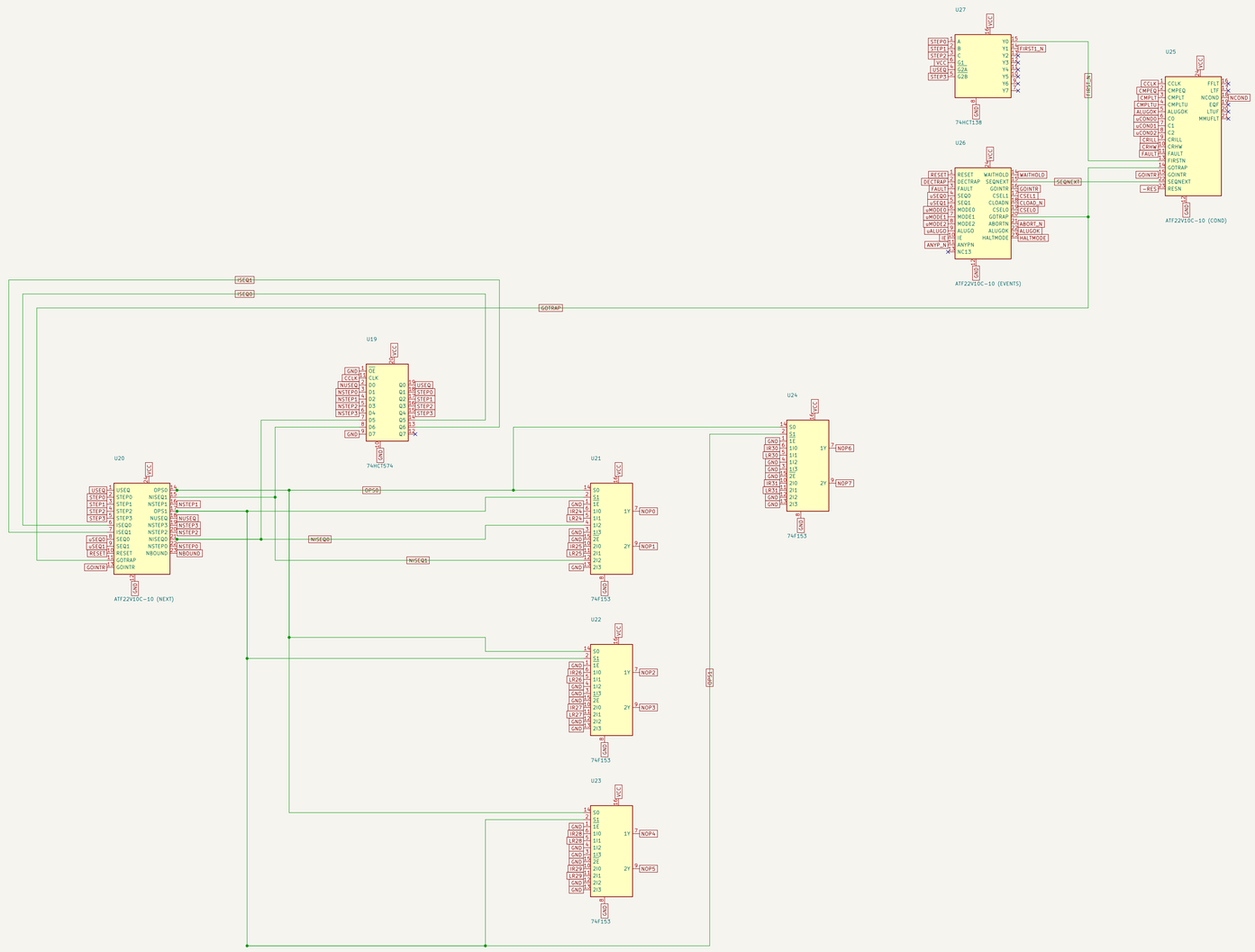
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Control store and the pipeline register

P32W

Sheet: /Control store and the pipeline register/
File: cpu-card-2.kicad_sch

Title: P32W CPU CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 3/18



Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit

Sequencer

P32W

Sheet: /Sequencer/

File: cpu-card-3.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 4/18

U28

74ABT377

U29

74ABT377

U30

74ABT377

U31

74ABT377

U32

SST39SF010A-55

U33

SST39SF010A-55

U34

74HCT4002

U35

74HCT4002

U36

74HCT4002

U37

74HCT4002

U38

ATF22V10C-10 (DECODE)

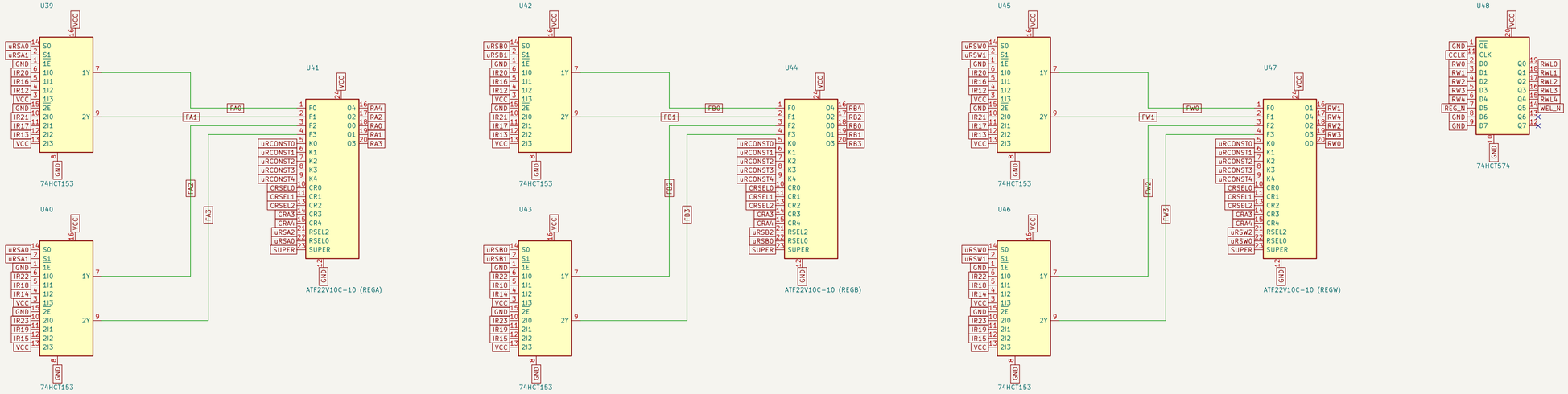
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Instruction register and decode

P32W

Sheet: /Instruction register and decode/
File: cpu-card-4.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 5/18



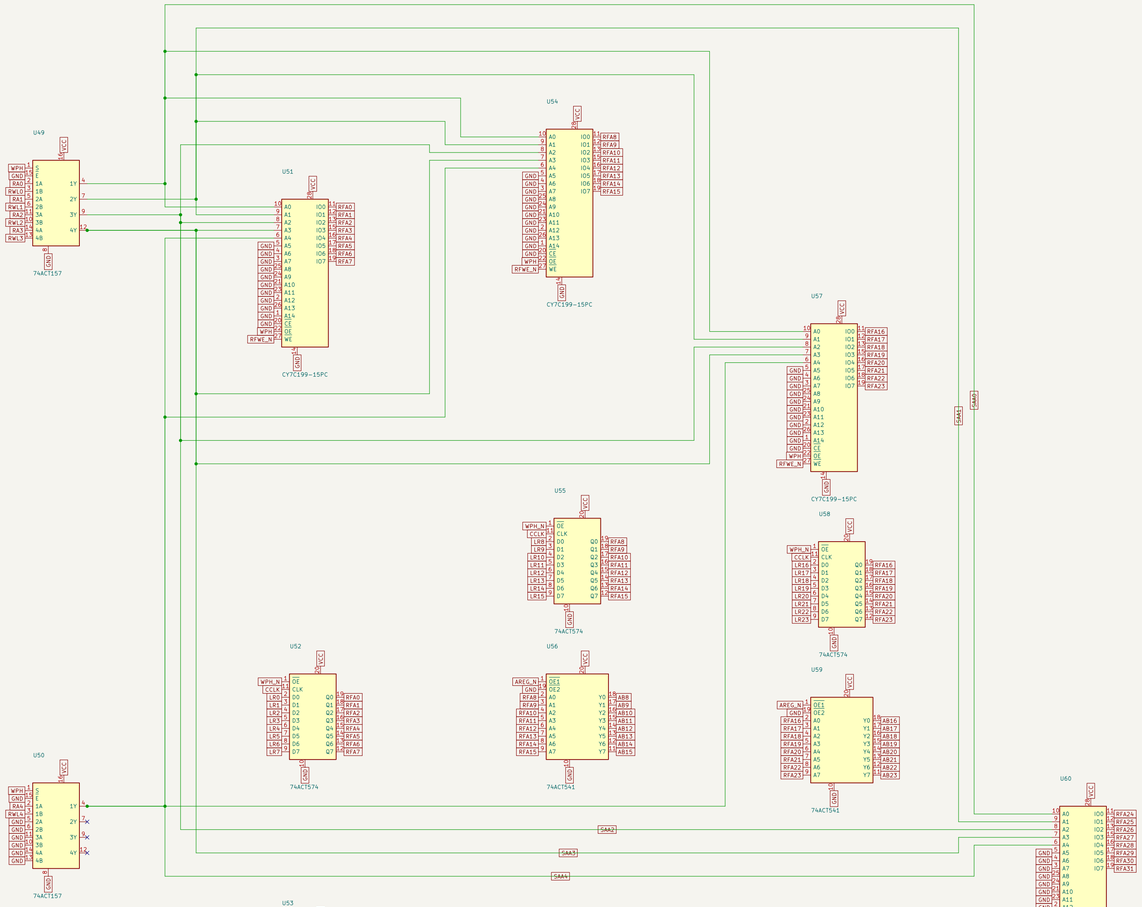
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Register addresses

P32W

Sheet: /Register addresses/
File: cpu-card-5.kicad_sch

Title: P32W CPU CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 6/18



Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit

Register file, copy A

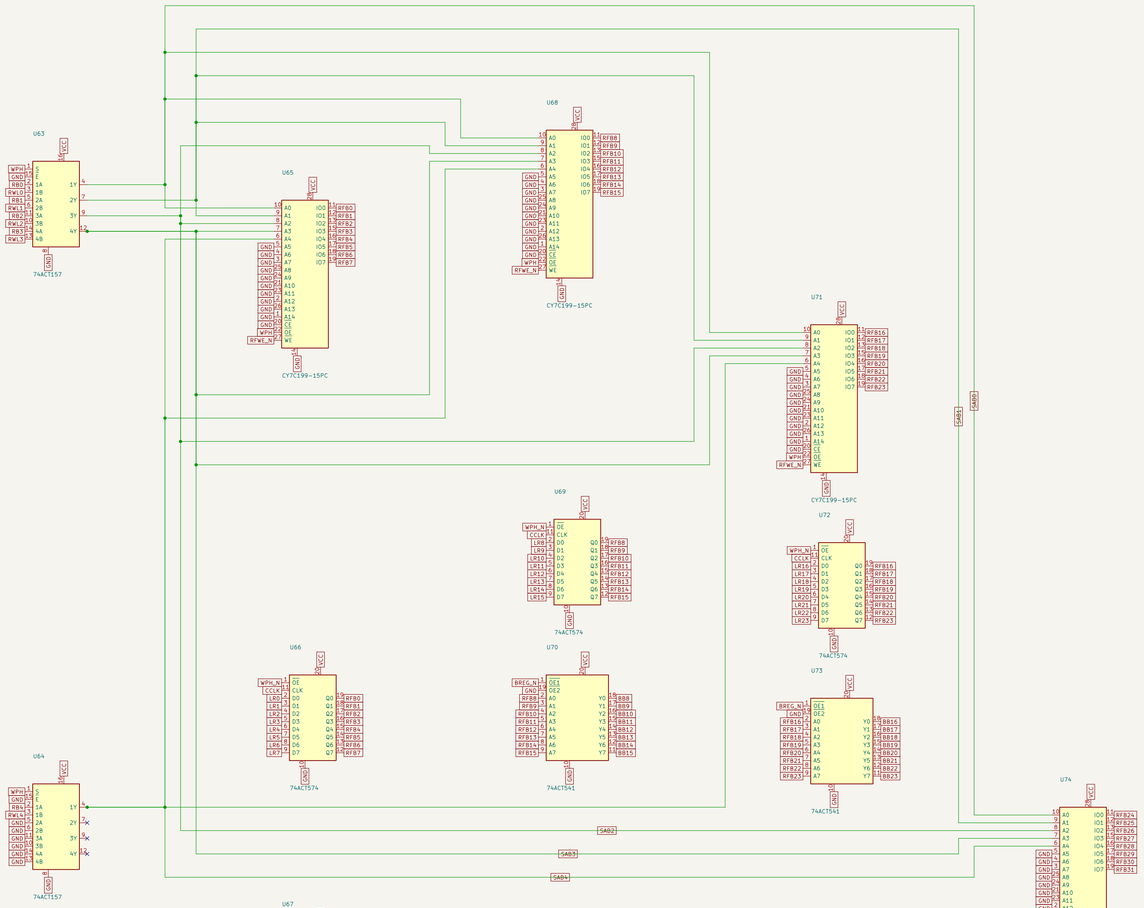
P32W

Sheet: /Register file, copy A/

File: cpu-card-6.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 7/18

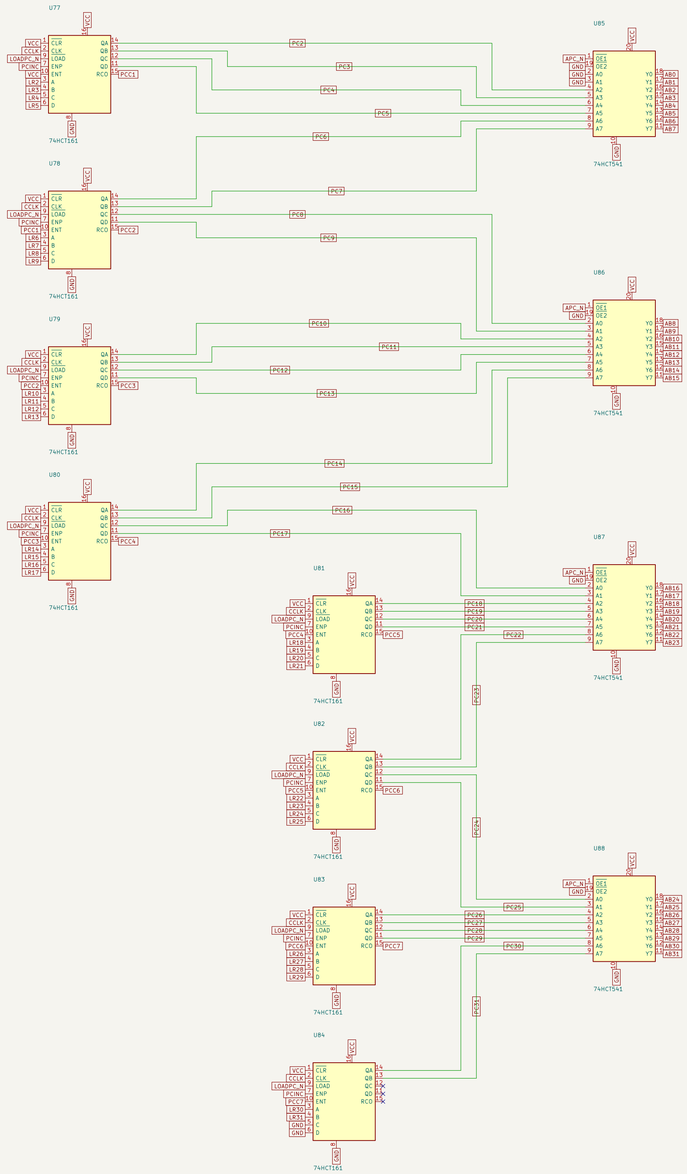


Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit

Register file, copy B

P32W

Sheet: /Register file, copy B/	
File: cpu-card-7.kicad_sch	
Title: P32W CPU CARD	
Size: A1	Date:
KiCad E.D.A. 10.0.6	**Rev:** Id: 8/18



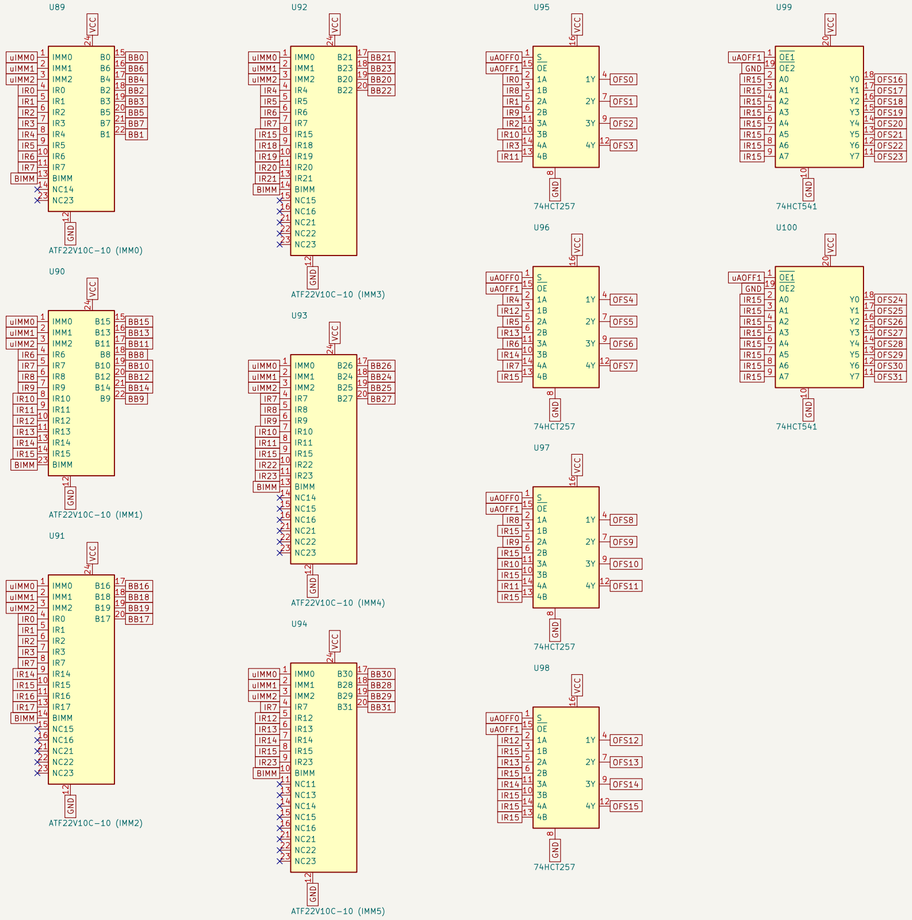
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
PC

P32W

Sheet: /PC/
File: cpu-card-B.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 9/18



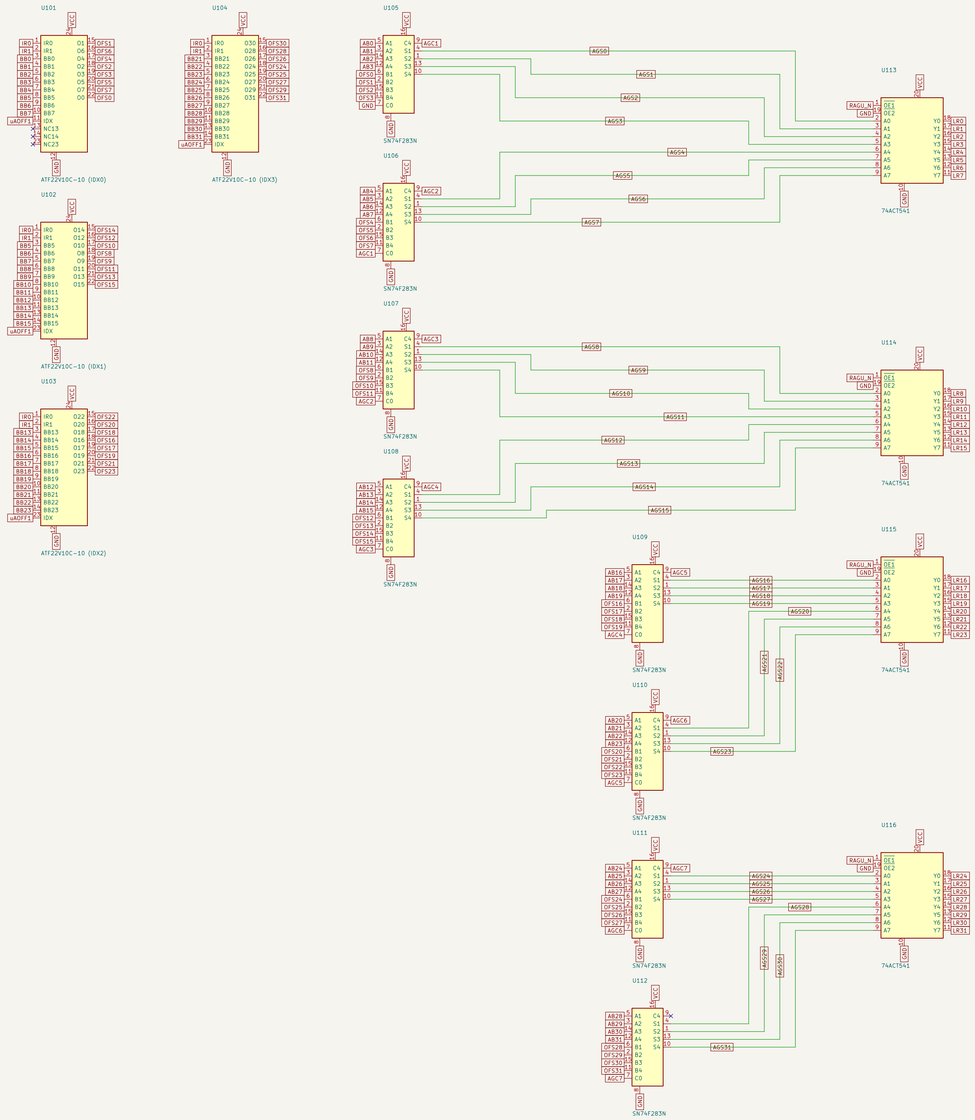
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Immediate unit and address offset

P32W

Sheet: /Immediate unit and address offset/
File: cpu-card-9.kicad_sch

Title: P32W CPU CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 10/18



Generated by generators/p32klcad_sch.py from generators/card_cpu.py — do not edit
Index and the address adder

P32W

Sheet: /Index and the address adder/
File: cpu-card-10.klcad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KICad E.D.A. 10.0.6		Id: 11/18

U117

ATF22V10C-10 (CMP0)

U118

ATF22V10C-10 (CMP1)

U119

ATF22V10C-10 (CMP2)

U120

ATF22V10C-10 (CMP3)

U121

ATF22V10C-10 (CMP4)

U122

ATF22V10C-10 (CMP5)

U123

ATF22V10C-10 (CMP6)

U124

ATF22V10C-10 (CMP7)

U125

ATF22V10C-10 (CMPH)

U126

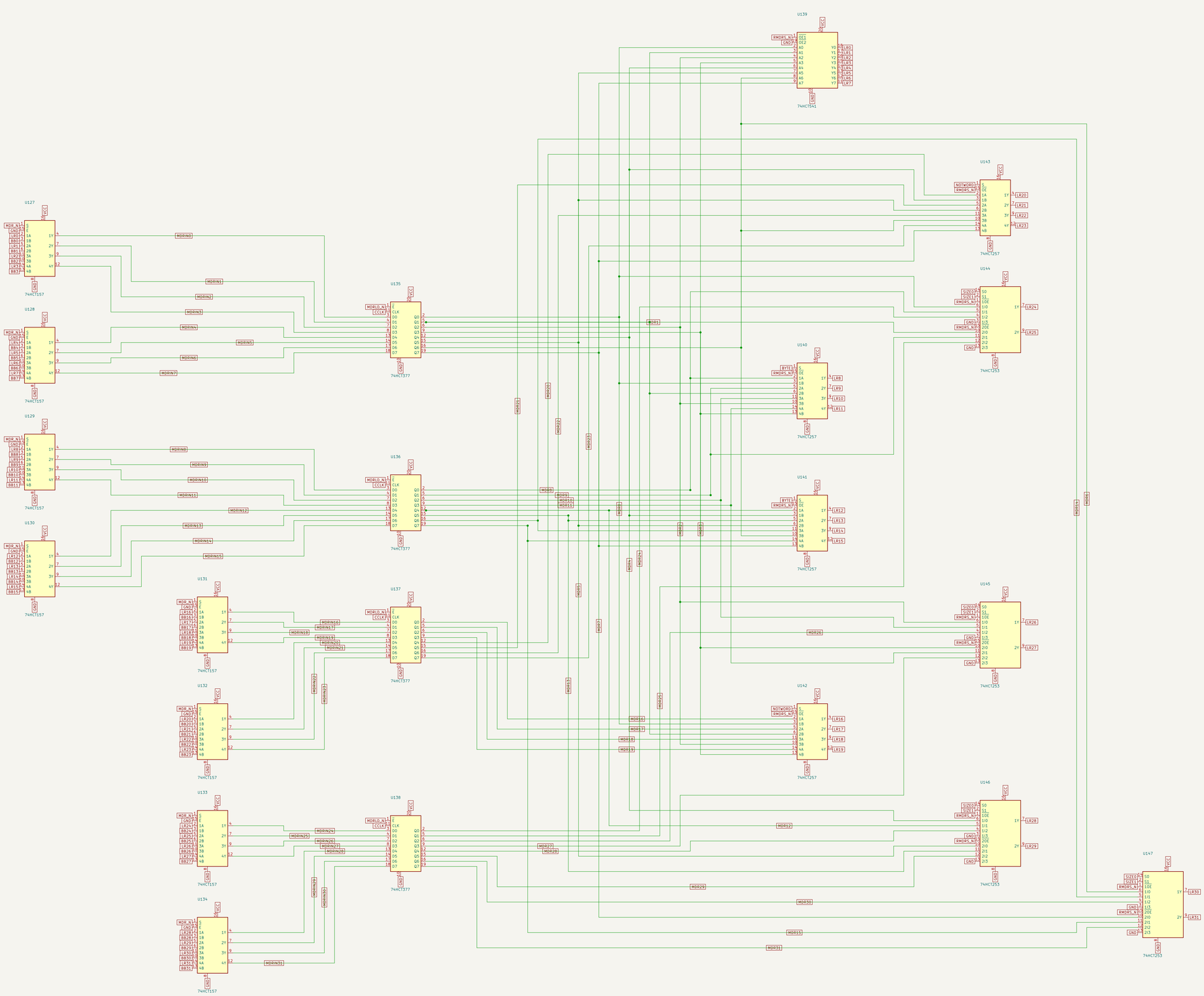
ATF22V10C-10 (CMPF)

CGT0
CLT0
CGT1
CLT1
CGT2
CLT2
CGT3
CLT3
CGT4
CLT4
CGT5
CLT5
CGT6
CLT6
CGT7
CLT7
CGTHIS
CGTHI
CLTHI
CLTHIS

Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Compare
P32W
Sheet: /Compare/
File: cpu-card-11.kicad_sch

Title: P32W CPU CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 12/18



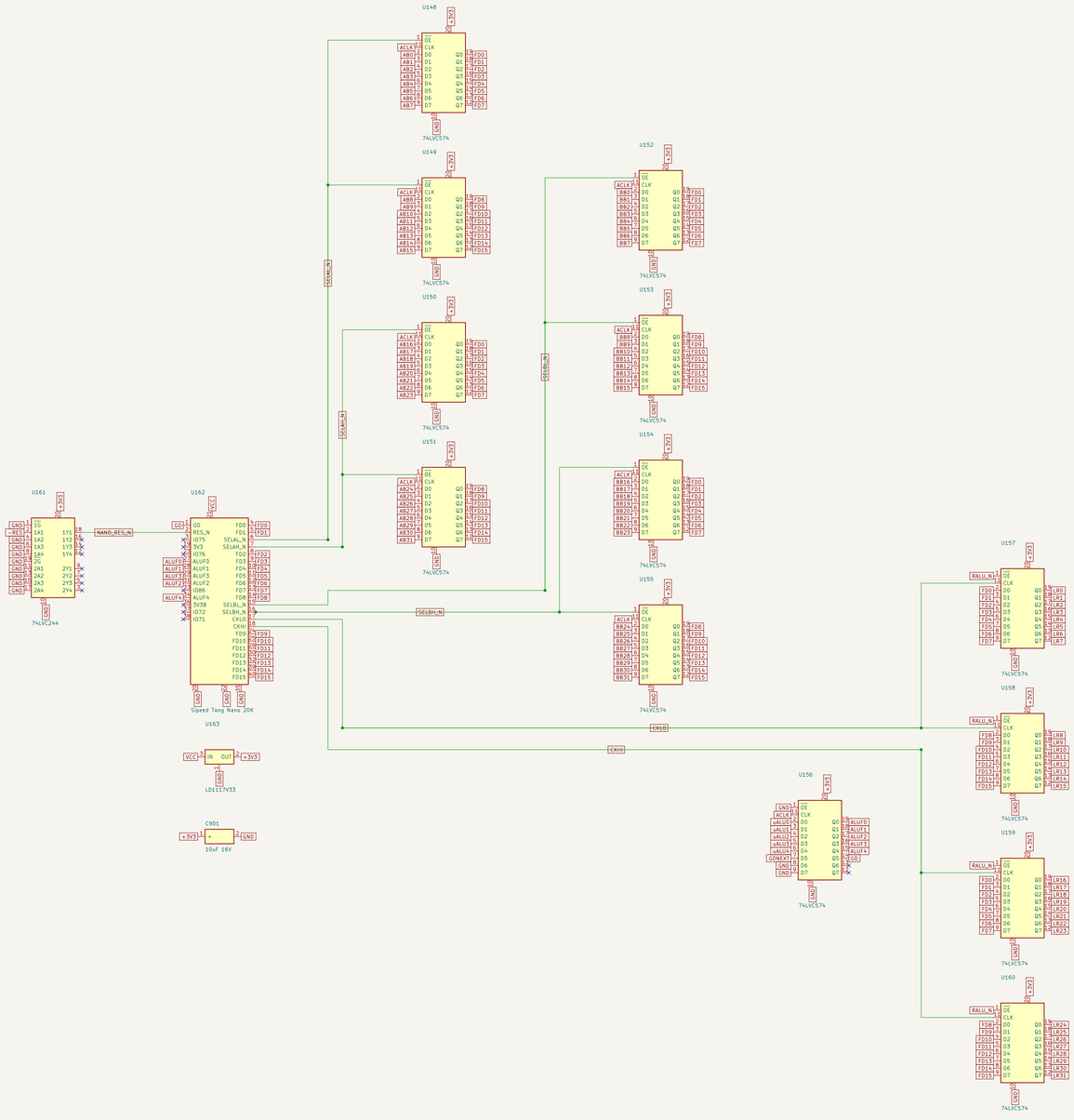
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
MDR and store data

P32W

Sheet: /MDR and store data/
File: cpu-card-12.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 13/18



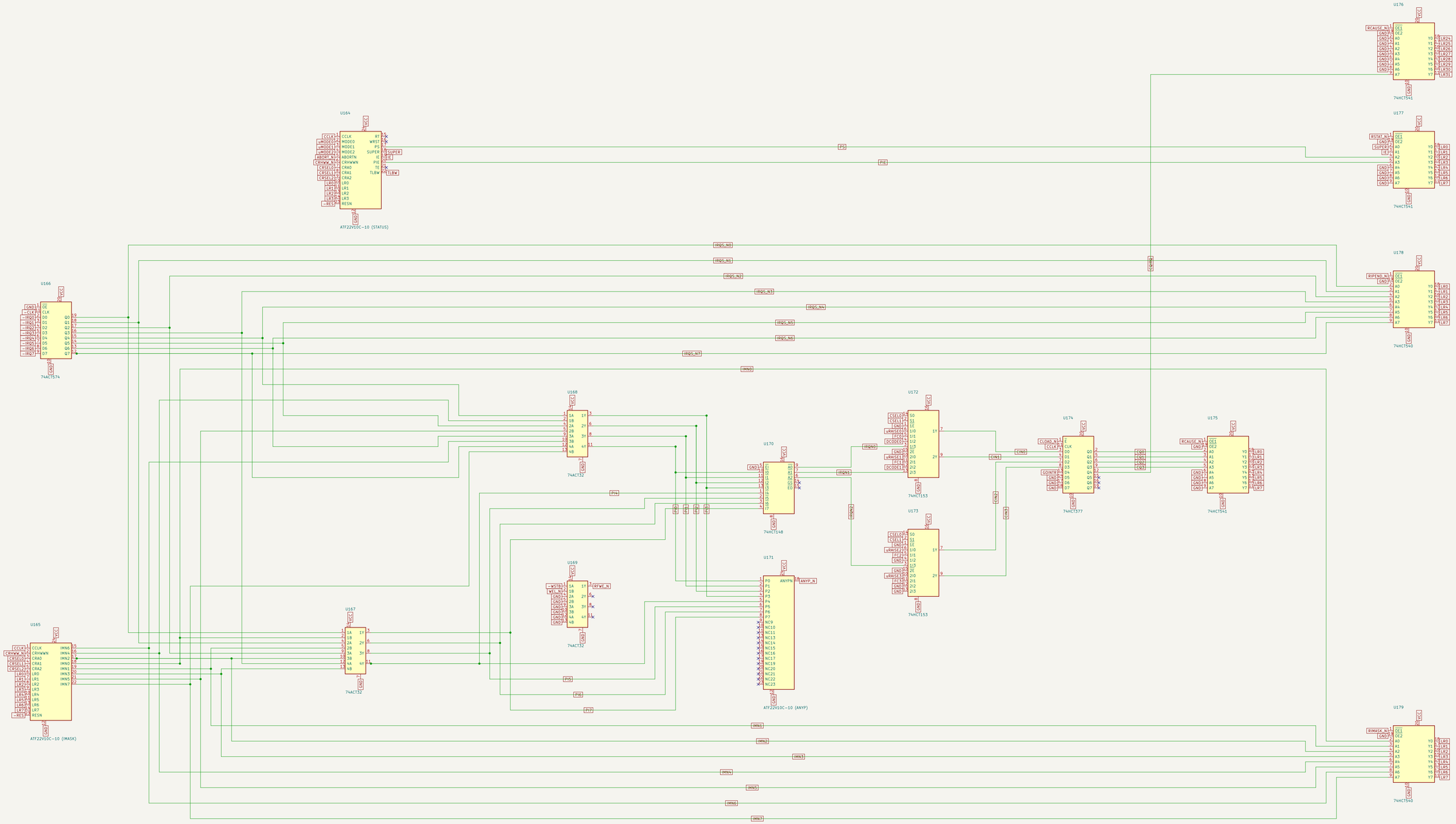
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
The ALU: Tang Nano 20K and its latches
P32W

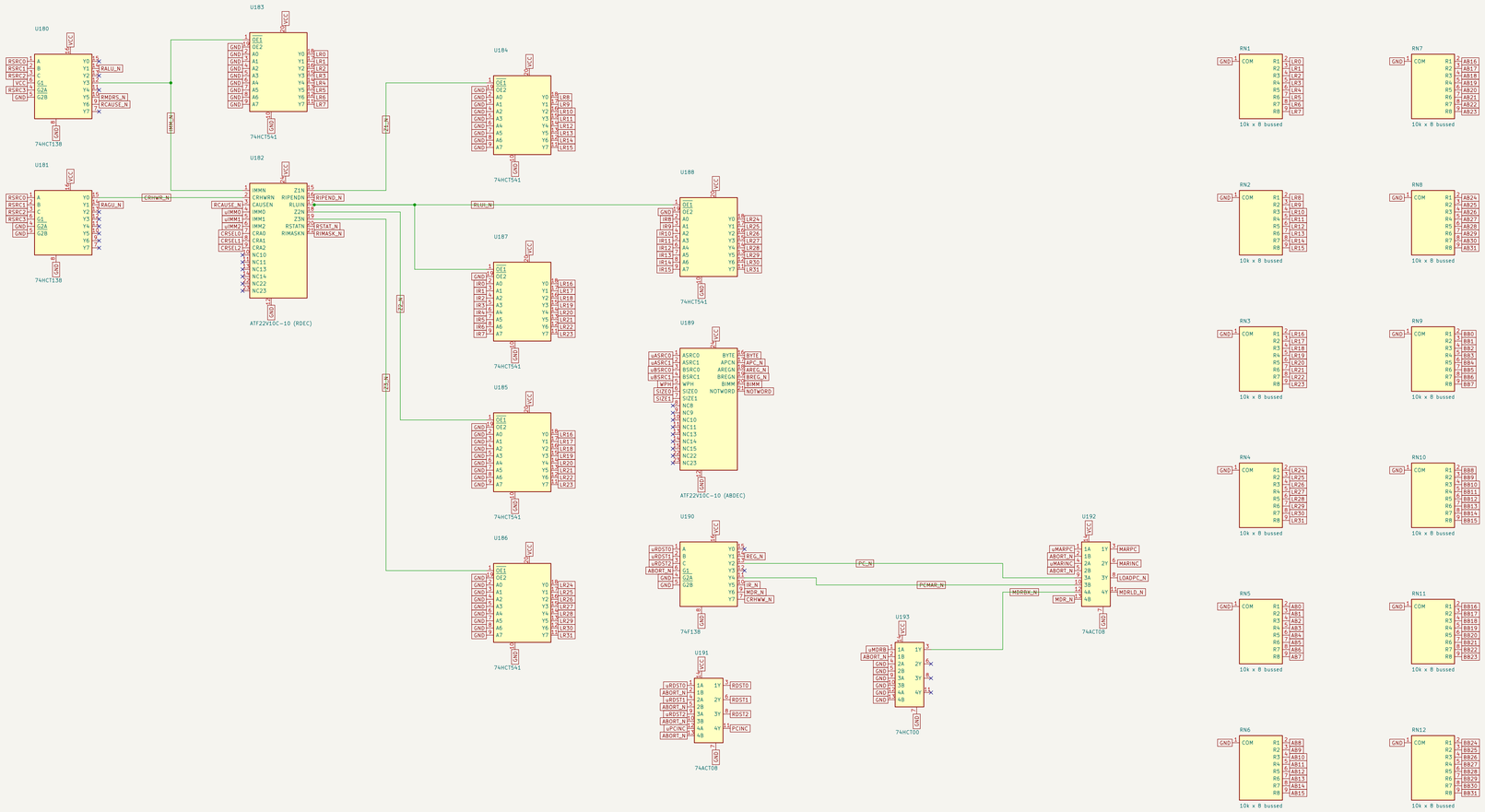
Sheet: /The ALU: Tang Nano 20K and its latches/

File: cpu-card-t3.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 14/18


U164
ATF22V10C-10 (STATUS)
U166
74ACT574
U165
ATF22V10C-10 (IMASK)
U167
74ACT32
U168
74ACT32
U169
74ACT32
U170
74HCT148
U171
ATF22V10C-10 (ANYP)
U172
74HCT153
U173
74HCT153
U174
74HCT377
U175
74HCT541
U176
74HCT541
U177
74HCT541
U178
74HCT540
U179
74HCT540
IRQS_N0
IRQS_N1
IRQS_N2
IRQS_N3
IRQS_N4
IRQS_N5
IRQS_N6
IRQS_N7
IMN0
IMN1
IMN2
IMN3
IMN4
IMN5
IMN6
IMN7
PS
PIE
P0
P1
P2
P4
P5
P6
P7
CIN0
CIN1
CIN2
CIN3
IRQN0
IRQN1
IRQN2
ANYP_N
RFWE_N
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Status, interrupts and the cause
P32W
Sheet: /Status, interrupts and the cause/
File: cpu-card-14.kicad_sch
Title: P32W CPU CARD
Size: A1
Date:
Rev:
KiCad E.D.A. 10.0.6
Id: 15/18



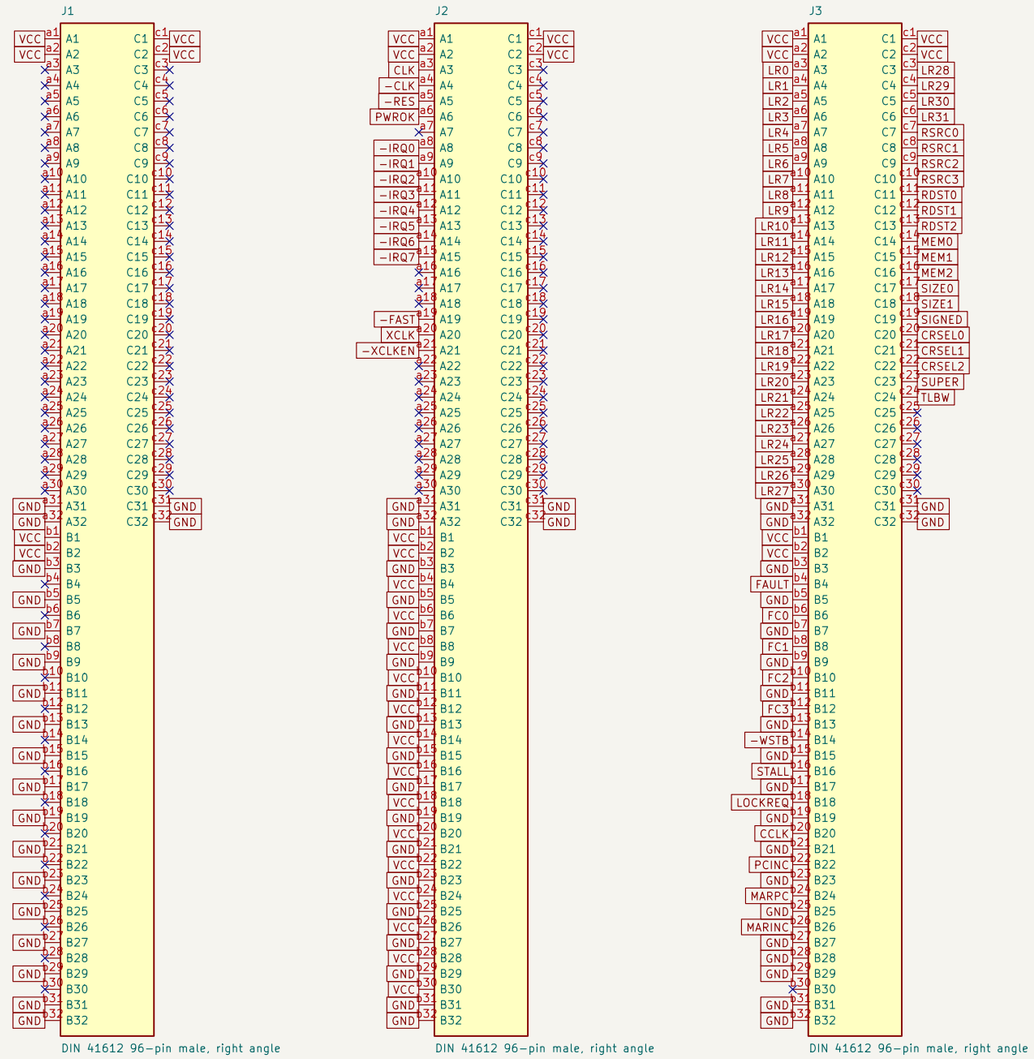
Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
R source and destination decode, zero fill, LUI

P32W

Sheet: /R source and destination decode, zero fill, LUI/
File: cpu-card-15.kicad_sch

Title: P32W CPU CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 16/18

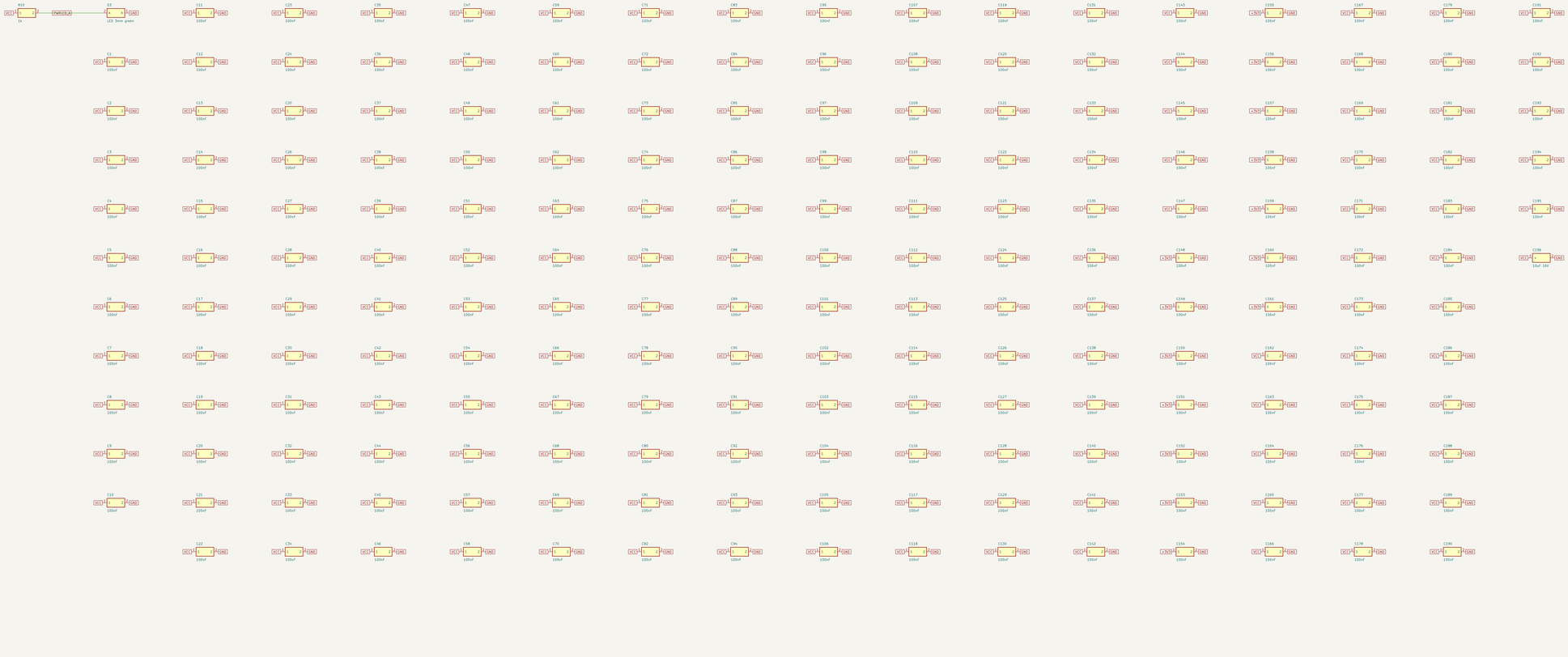


Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Backplane connectors
P32W

Sheet: /Backplane connectors/
File: cpu-card-16.kicad_sch

Title: P32W CPU CARD

Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 17/18



Generated by generators/p32kicad_sch.py from generators/card_cpu.py — do not edit
Power decoupling and bulk capacitors
P32W
Sheet: /Power decoupling and bulk capacitors/
File: cpu-card-17.kicad_sch

Title: P32W CPU CARD

Size: A3	Date:	Rev:
KiCad E.D.A. 10.0.0		id: 18/18