Bus interface: decode, address and data buffers

File: mem-card-1.kicad_sch

RAM banks 0 and 1

File: mem-card-2.kicad_sch

RAM banks 2 and 3

File: mem-card-3.kicad_sch

RAM banks 4 and 5

File: mem-card-4.kicad_sch

RAM banks 6 and 7, the boot ROM

File: mem-card-5.kicad_sch

Backplane connectors

File: mem-card-6.kicad_sch

Generated by generators/p32kicad_sch.py — do not edit
Root sheet: one block per functional sheet
P32W

Sheet: /
File: mem-card.kicad_sch

Title: P32W MEMORY CARD

Size: A4	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 1/8

U1

ATF22V10C-10 (MSEL)

RN1

10k x 8 bussed

JP3

jumper

JP2

jumper

JP1

jumper

JP4

jumper

JP5

jumper

JP6

jumper

U3

ATF22V10C-10 (MWE)

U2

ATF22V10C-10 (MBANK)

U11

74HCT138

U4

74HCT541

U5

74HCT541

U6

74HCT541

U48

74HC05

U7

74HCT245

U8

74HCT245

U9

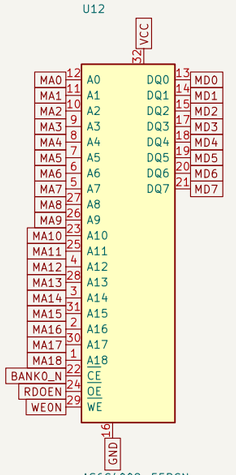
74HCT245

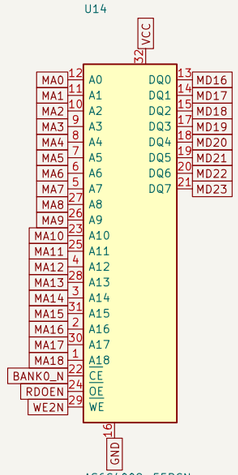
U10

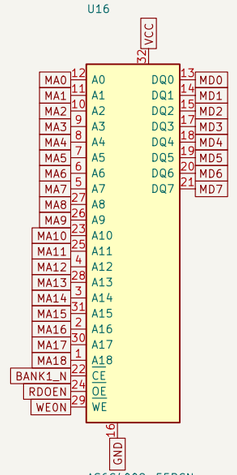
74HCT245

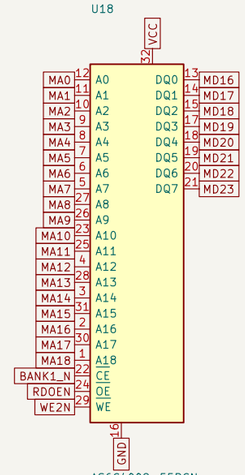
Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit
Bus interface: decode, address and data buffers
P32W

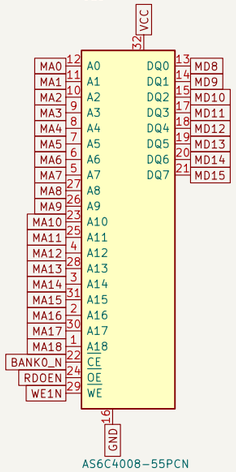
Sheet: /Bus interface: decode, address and data buffers/	
File: mem-card-1.kicad_sch	
Title: P32W MEMORY CARD	
Size: A1	Date:
KiCad E.D.A. 10.0.6	Id: 2/8

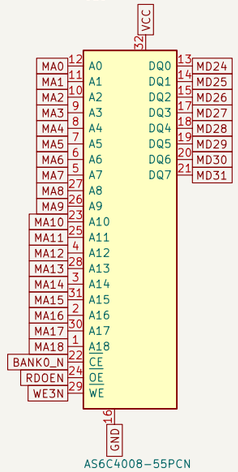


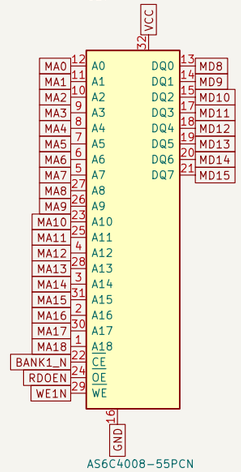


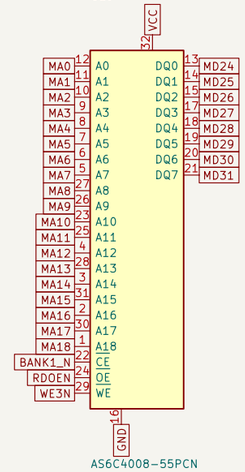












Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit

RAM banks 0 and 1

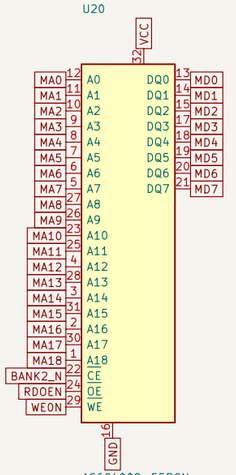
P32W

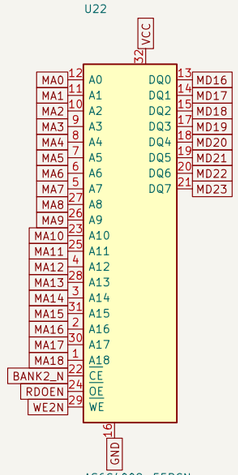
Sheet: /RAM banks 0 and 1/

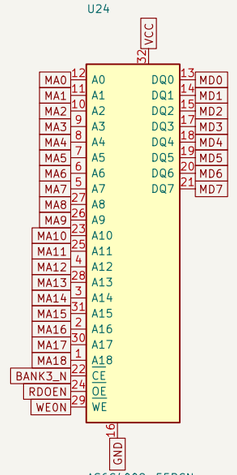
File: mem-card-2.kicad_sch

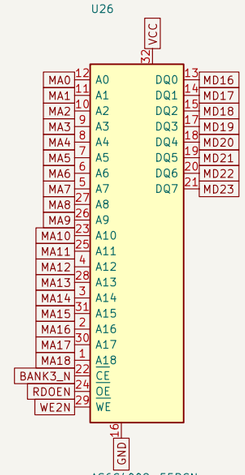
Title: P32W MEMORY CARD

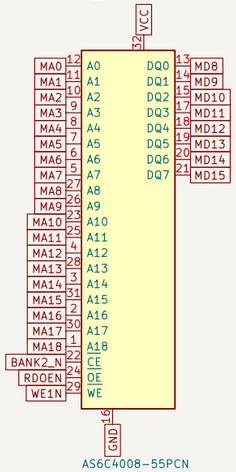
Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 3/8

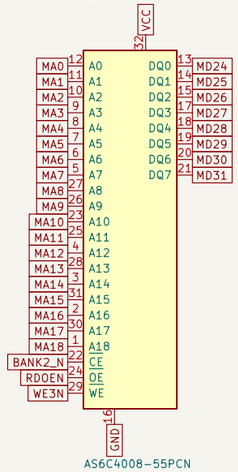


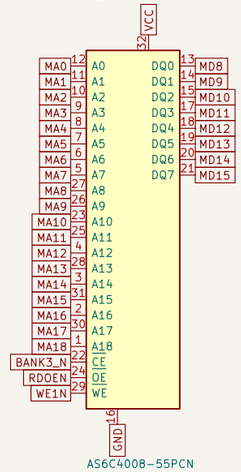


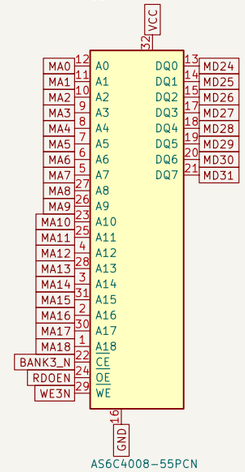












Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit

RAM banks 2 and 3

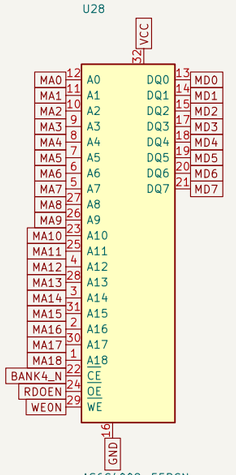
P32W

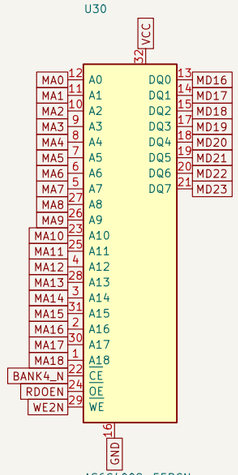
Sheet: /RAM banks 2 and 3/

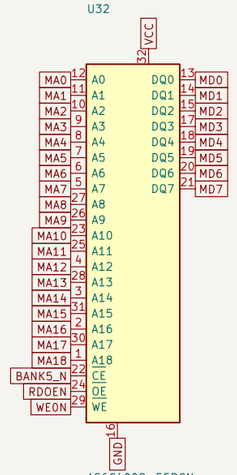
File: mem-card-3.kicad_sch

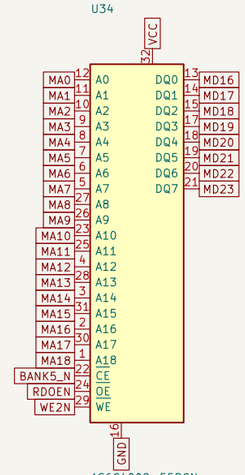
Title: P32W MEMORY CARD

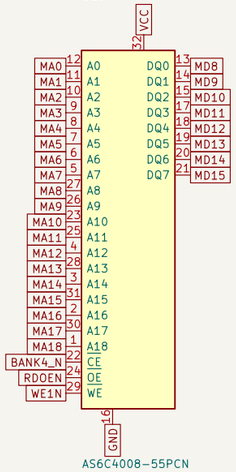
Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 4/8

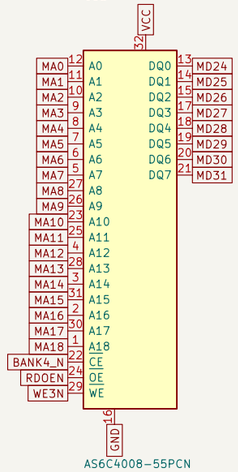


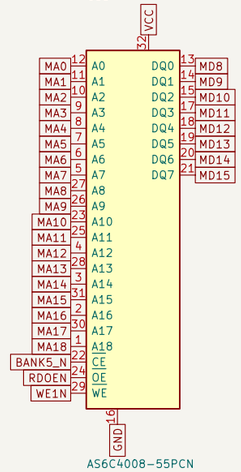


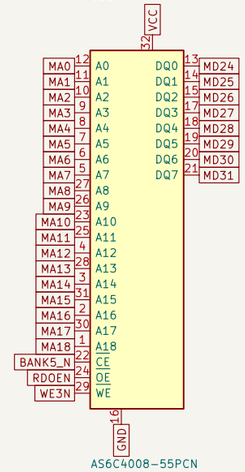












Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit

RAM banks 4 and 5

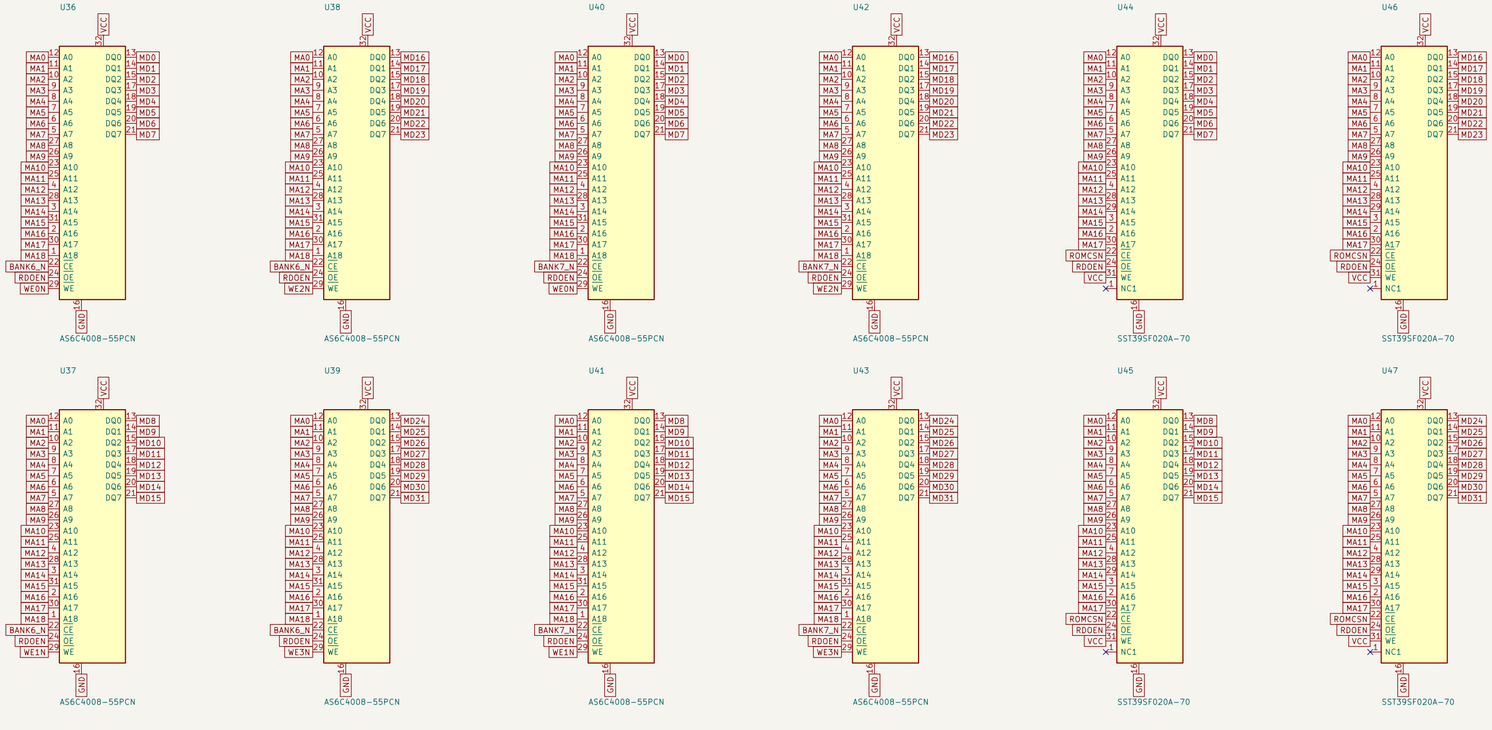
P32W

Sheet: /RAM banks 4 and 5/

File: mem-card-4.kicad_sch

Title: P32W MEMORY CARD

Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 5/8



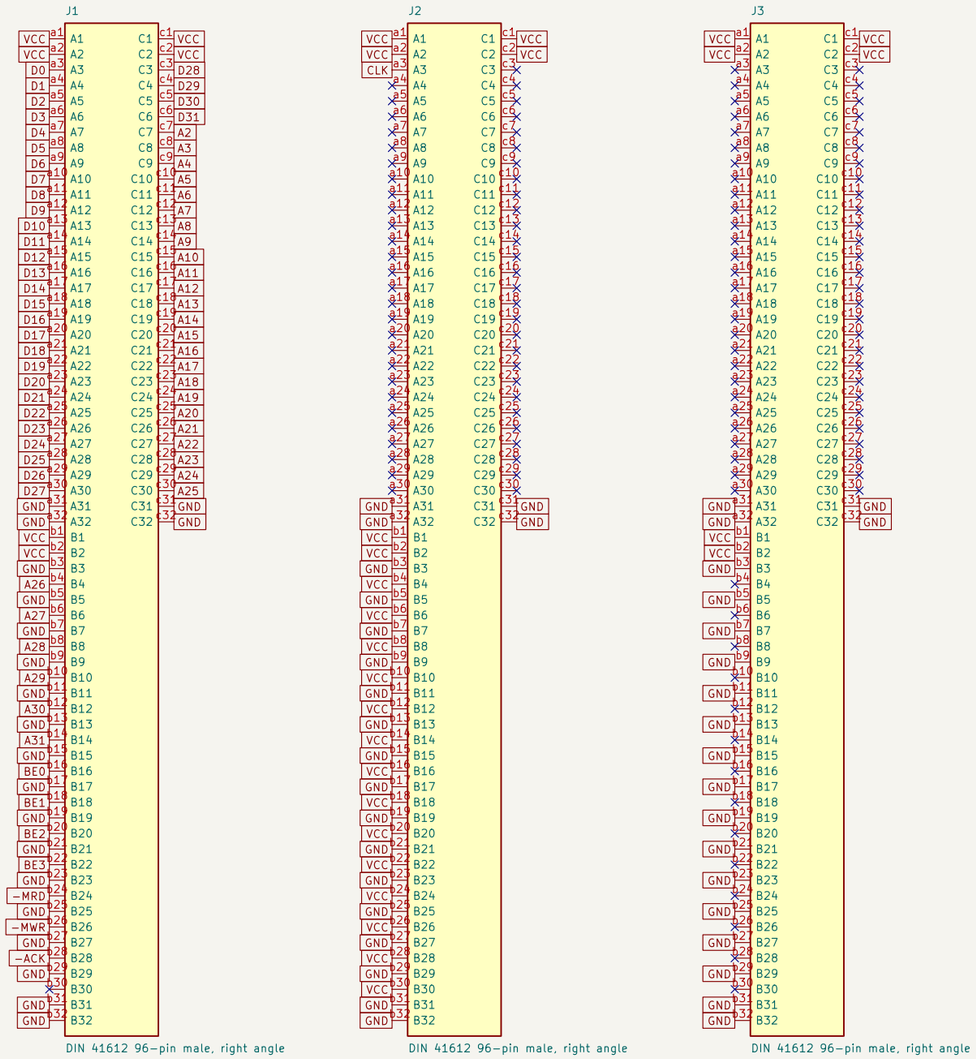
Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit
RAM banks 6 and 7, the boot ROM

P32W

Sheet: /RAM banks 6 and 7, the boot ROM/
File: mem-card-5.kicad_sch

Title: P32W MEMORY CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 6/8

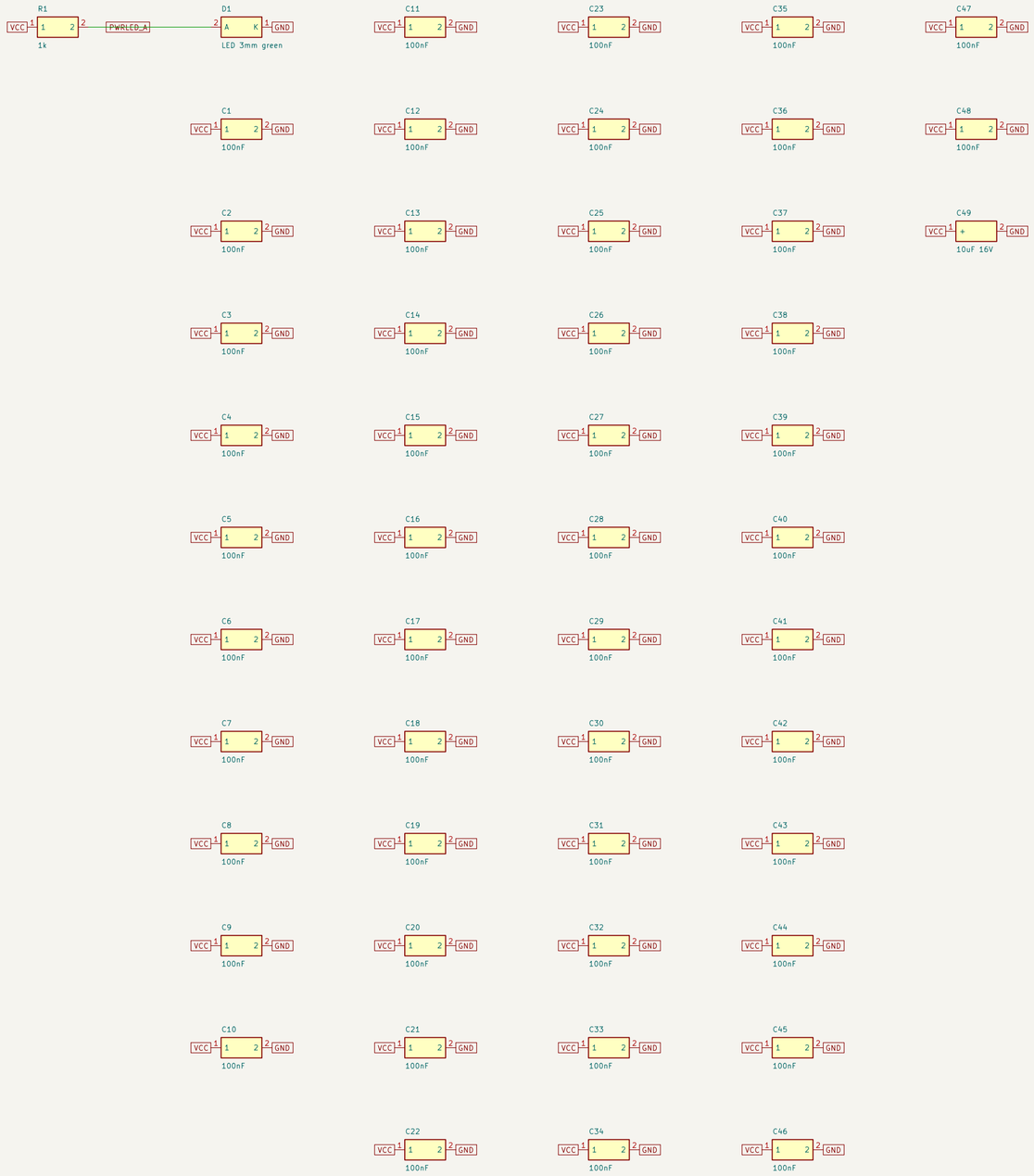


Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit
Backplane connectors
P32W

Sheet: /Backplane connectors/
File: mem-card-6.kicad_sch

Title: P32W MEMORY CARD

Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 7/8



Generated by generators/p32kicad_sch.py from generators/card_mem.py — do not edit
Power: decoupling and bulk capacitors
P32W
Sheet: /Power: decoupling and bulk capacitors/
File: mem-card-7.kicad_sch

Title: P32W MEMORY CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 8/8