MAR: its input and its counters

File: mmu-card-1.kicad_sch

The shadow PC

File: mmu-card-2.kicad_sch

TLB: index and entries

File: mmu-card-3.kicad_sch

TLB: write data, tag compare, match

File: mmu-card-4.kicad_sch

Faults

File: mmu-card-5.kicad_sch

Physical address, bus address, store data

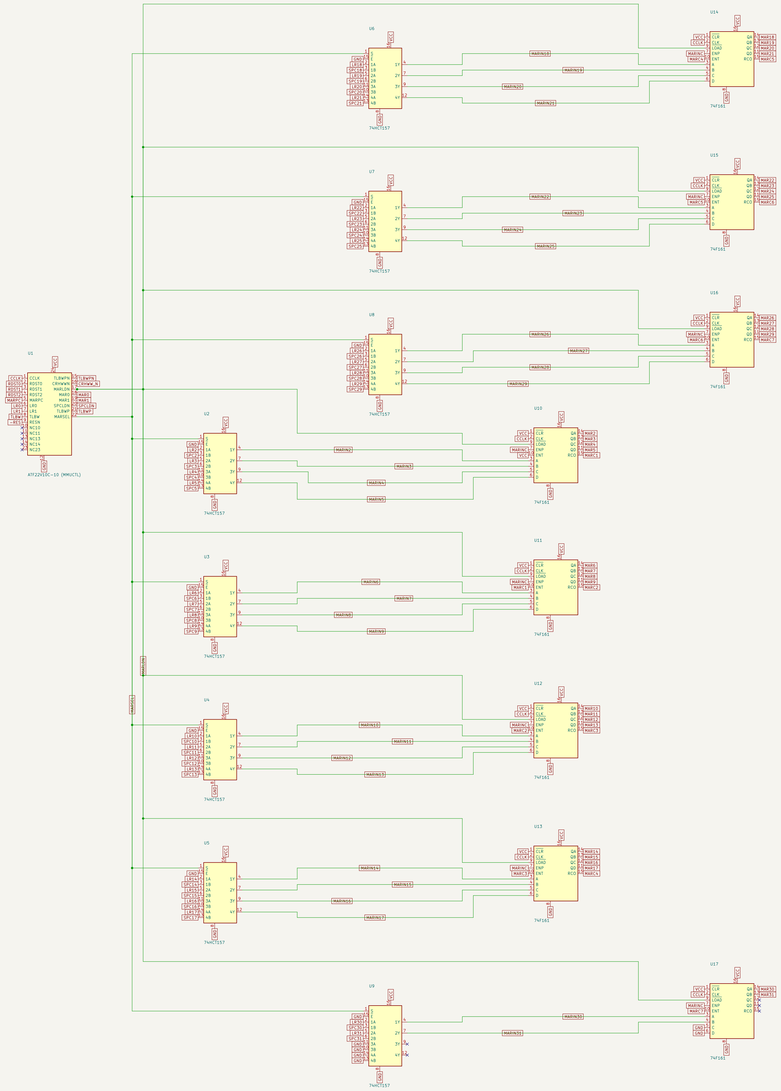
File: mmu-card-6.kicad_sch

Generated by generators/p32kicad_sch.py — do not edit
Root sheet: one block per functional sheet
P32W

Sheet: /
File: mmu-card.kicad_sch

Title: P32W MMU / DATA CARD

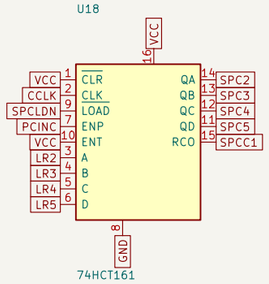
Size: A4	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 1/14

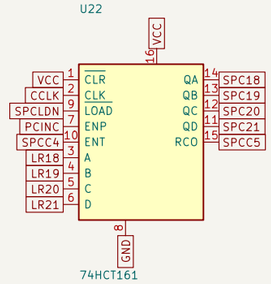


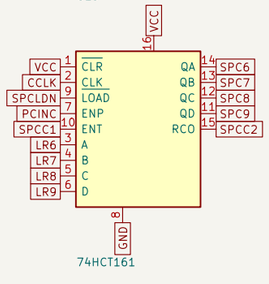
Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
MAR: 7b input and its counters
P32W
Sheet: /MAR: 7b input and its counters/
File: mmu-card-1.kicad_sch

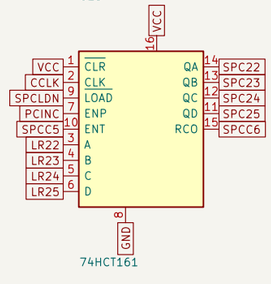
Title: P32W MMU / DATA CARD

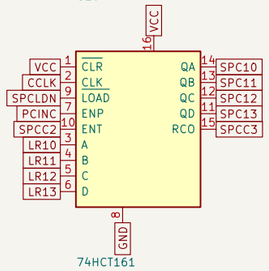
Size: A3	Date:	Rev:
KiCad E.D.A. 10.0.0		id: 2/14

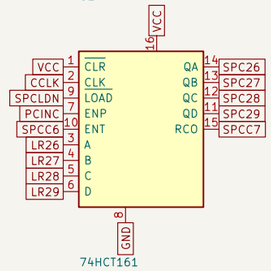


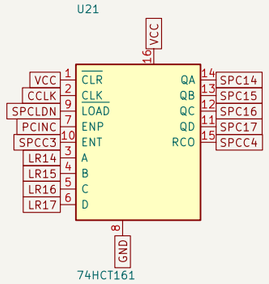


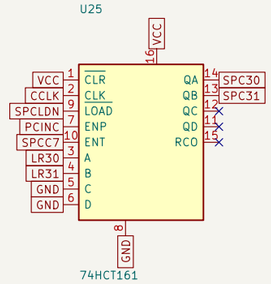










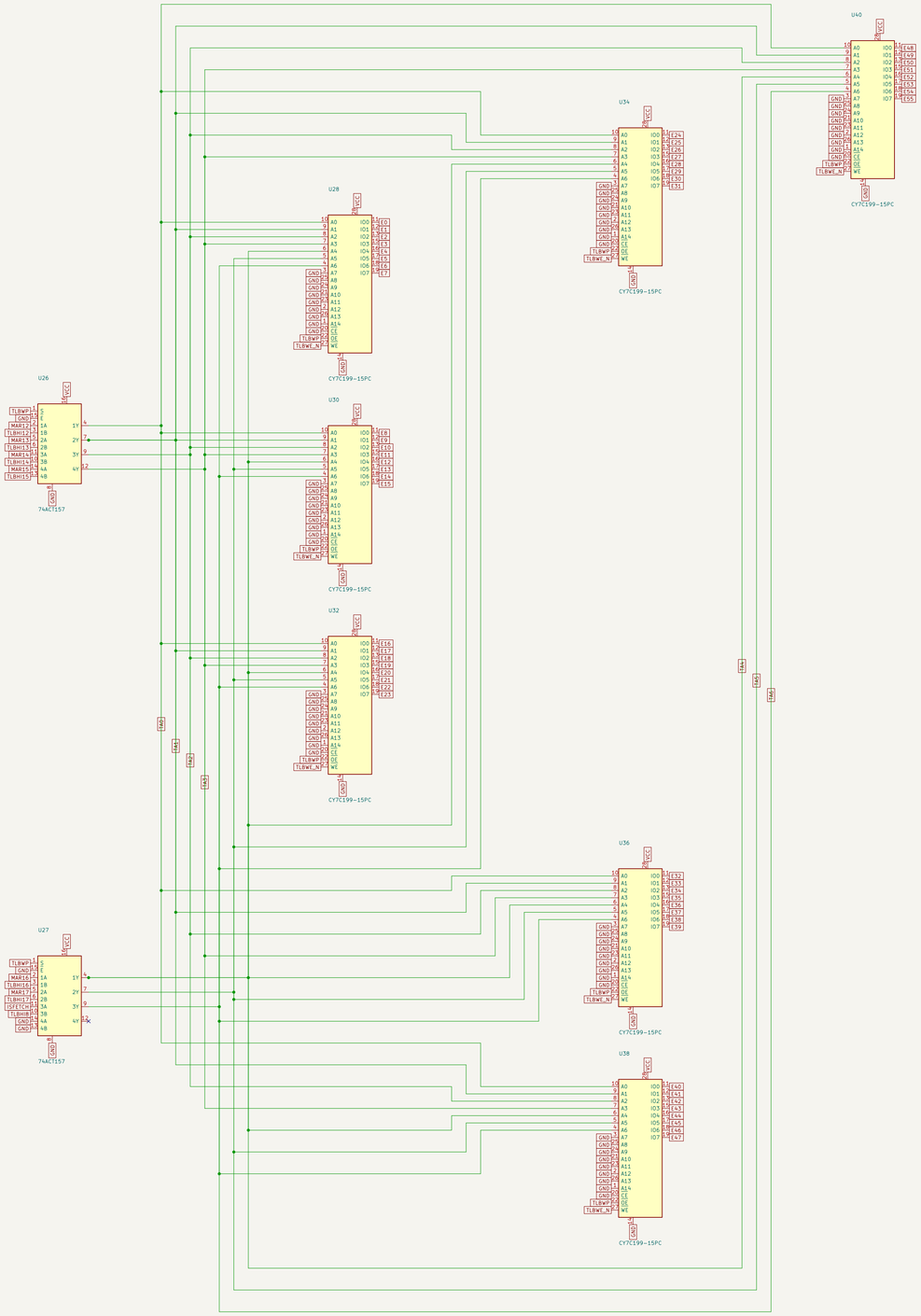


Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
The shadow PC
P32W

Sheet: /The shadow PC/
File: mmu-card-2.kicad_sch

Title: P32W MMU / DATA CARD

Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 3/14



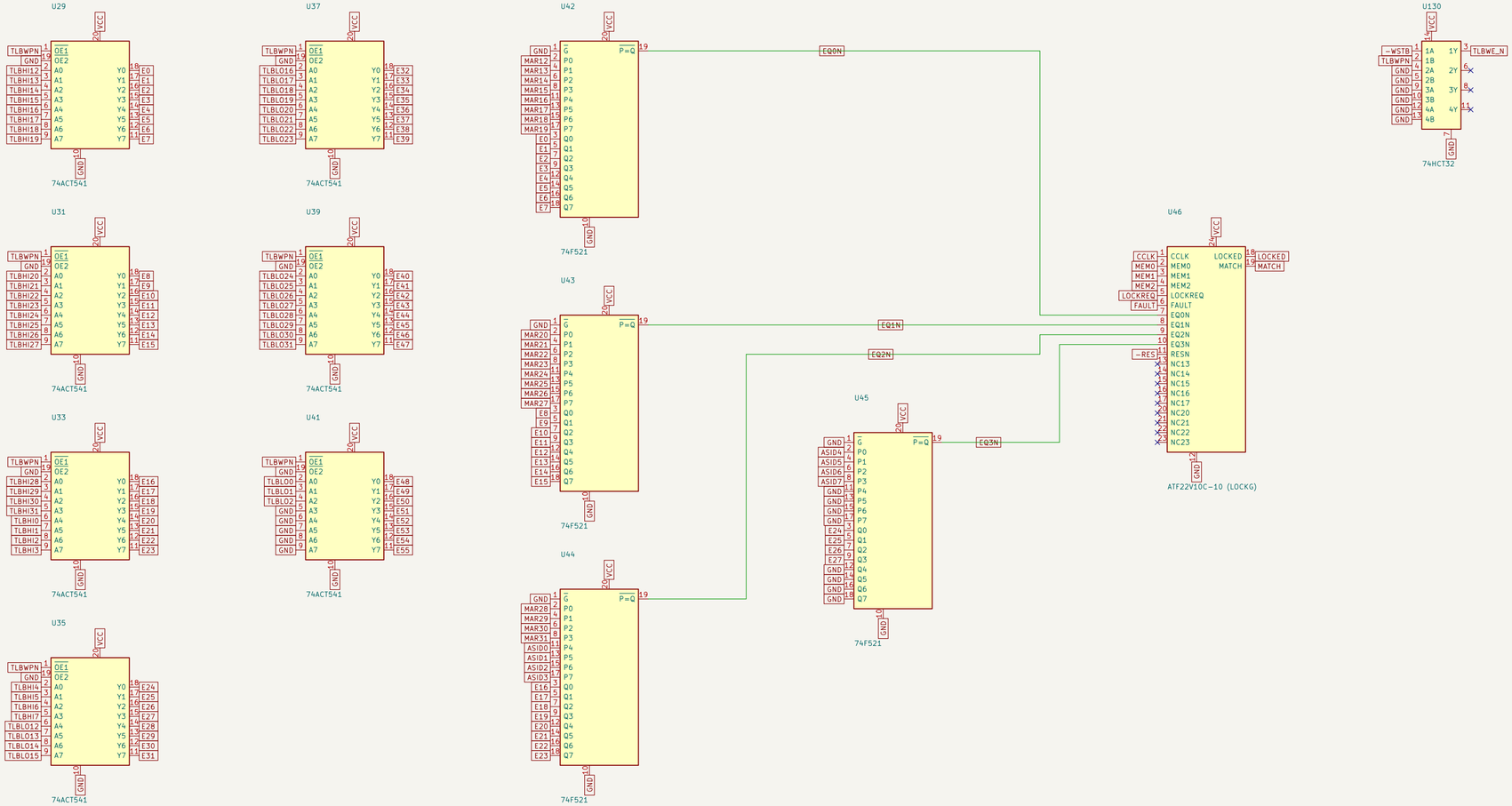
Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
TLB: index and entries

P32W

Sheet: /TLB: Index and entries/
File: mmu-card-3.kicad_sch

Title: P32W MMU / DATA CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 4/14



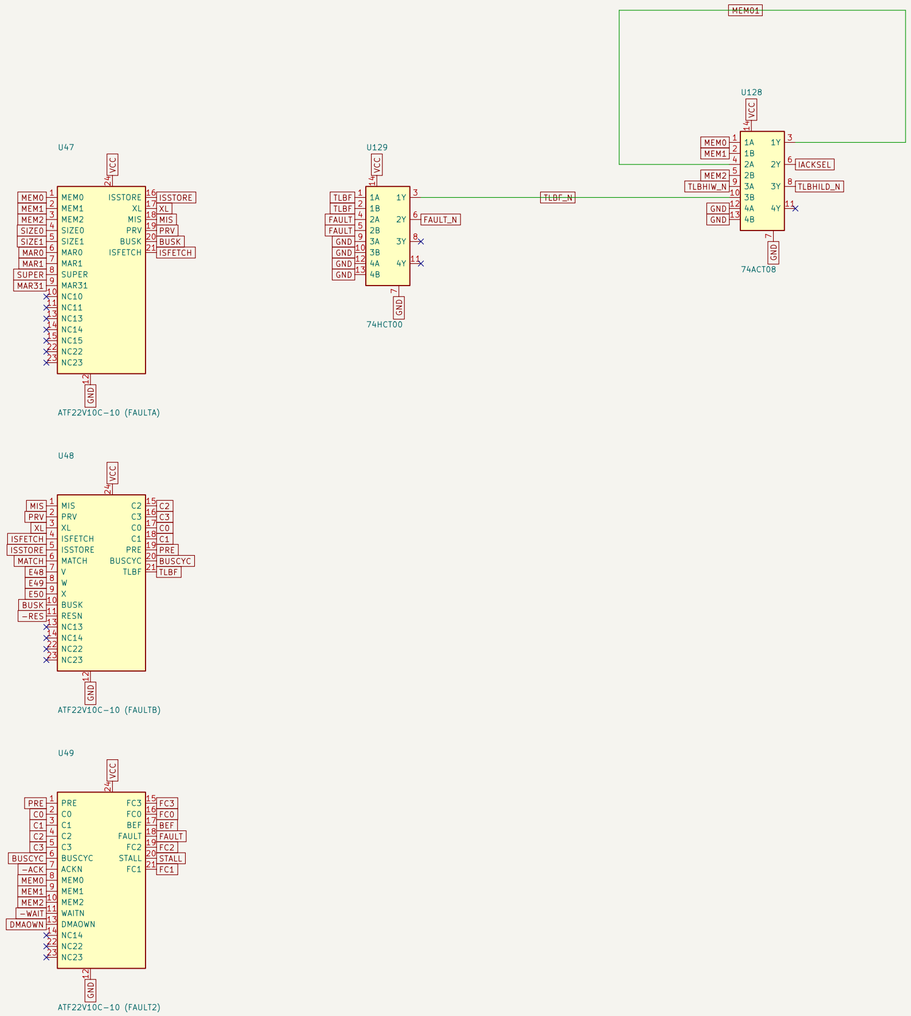
Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
TLB: write data, tag compare, match

P32W

Sheet: /TLB: write data, tag compare, match/
File: mmu-card-4.kicad_sch

Title: P32W MMU / DATA CARD

Size: A2	Date:	**Rev:**
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Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit

Faults

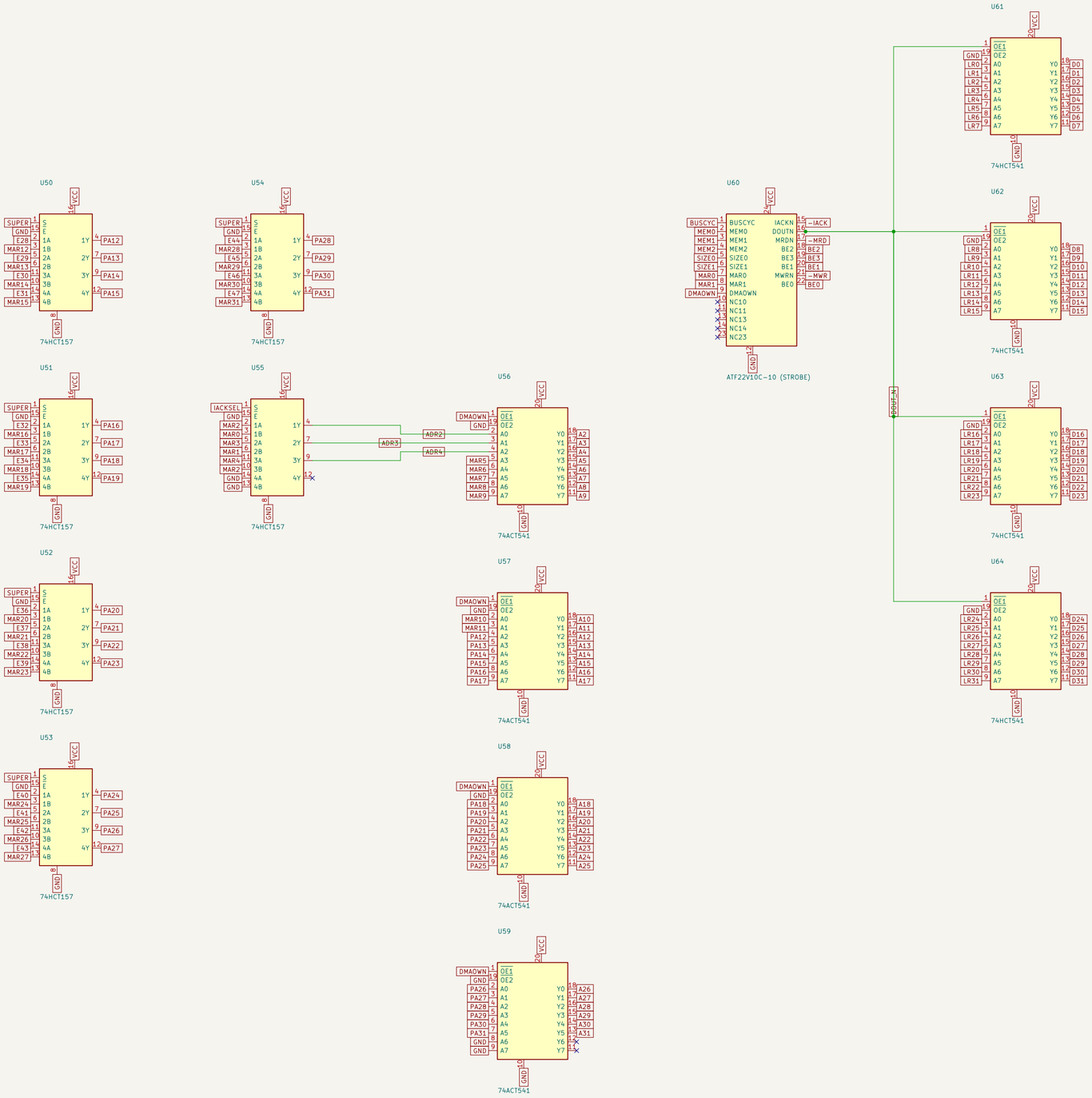
P32W

Sheet: /Faults/

File: mmu-card-5.kicad_sch

Title: P32W MMU / DATA CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 6/14



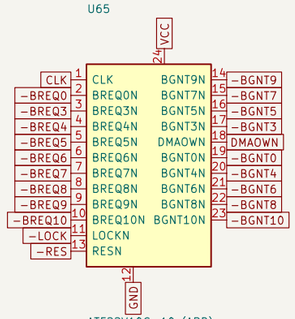
Generated by generators/p32kicad_sch.py from generators/card_mmu.py – do not edit
Physical address, bus address, store data

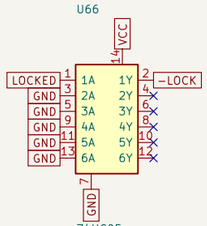
P32W

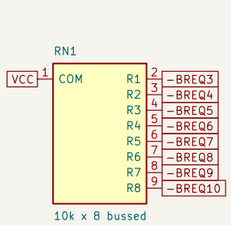
Sheet: /Physical address, bus address, store data/
File: mmu-card-6.kicad_sch

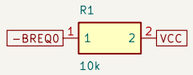
Title: P32W MMU / DATA CARD

Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 7/14







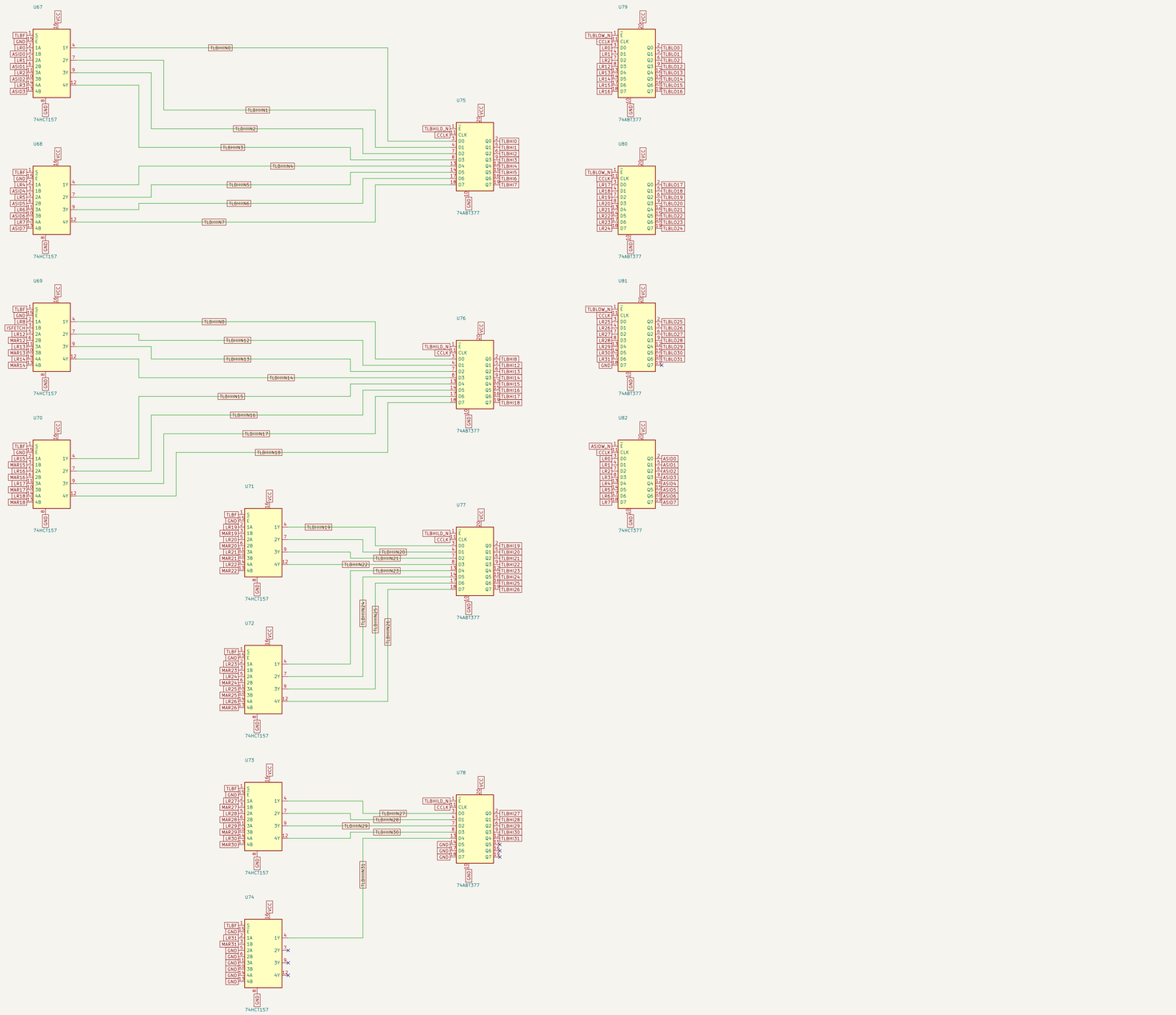


Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
The bus arbiter
P32W

Sheet: /The bus arbiter/
File: mmu−card−7.kicad_sch

Title: P32W MMU / DATA CARD

Size: A3	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 8/14



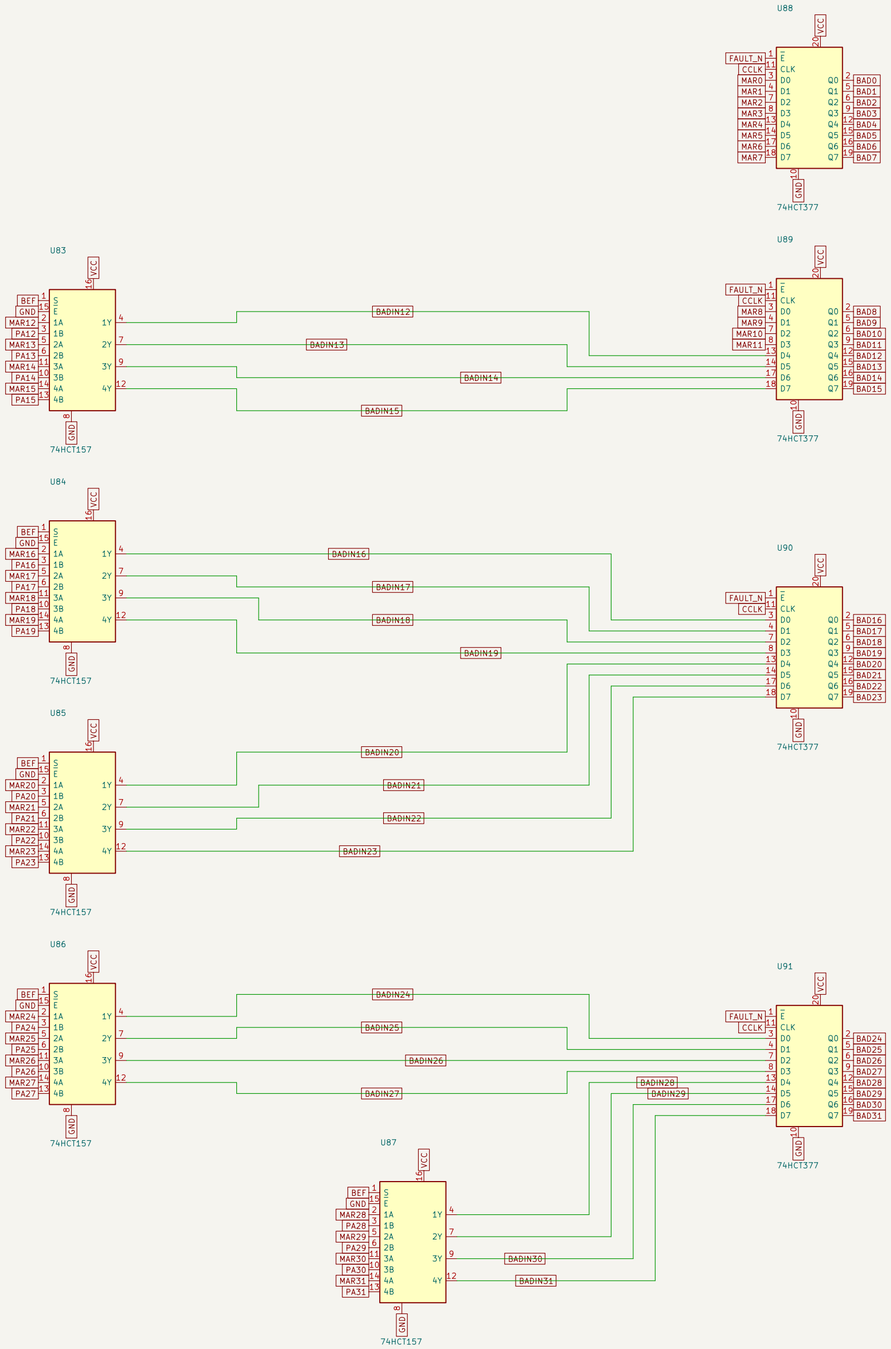
Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
TLBHI, TLBLO and ASID

P32W

Sheet: /TLBHI, TLBLO and ASID/
File: mmu-card-B.kicad_sch

Title: P32W MMU / DATA CARD

Size: A1	Date:	Rev:
KiCad E.D.A. 10.0.6		Id: 9/14



Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit

BADADDR

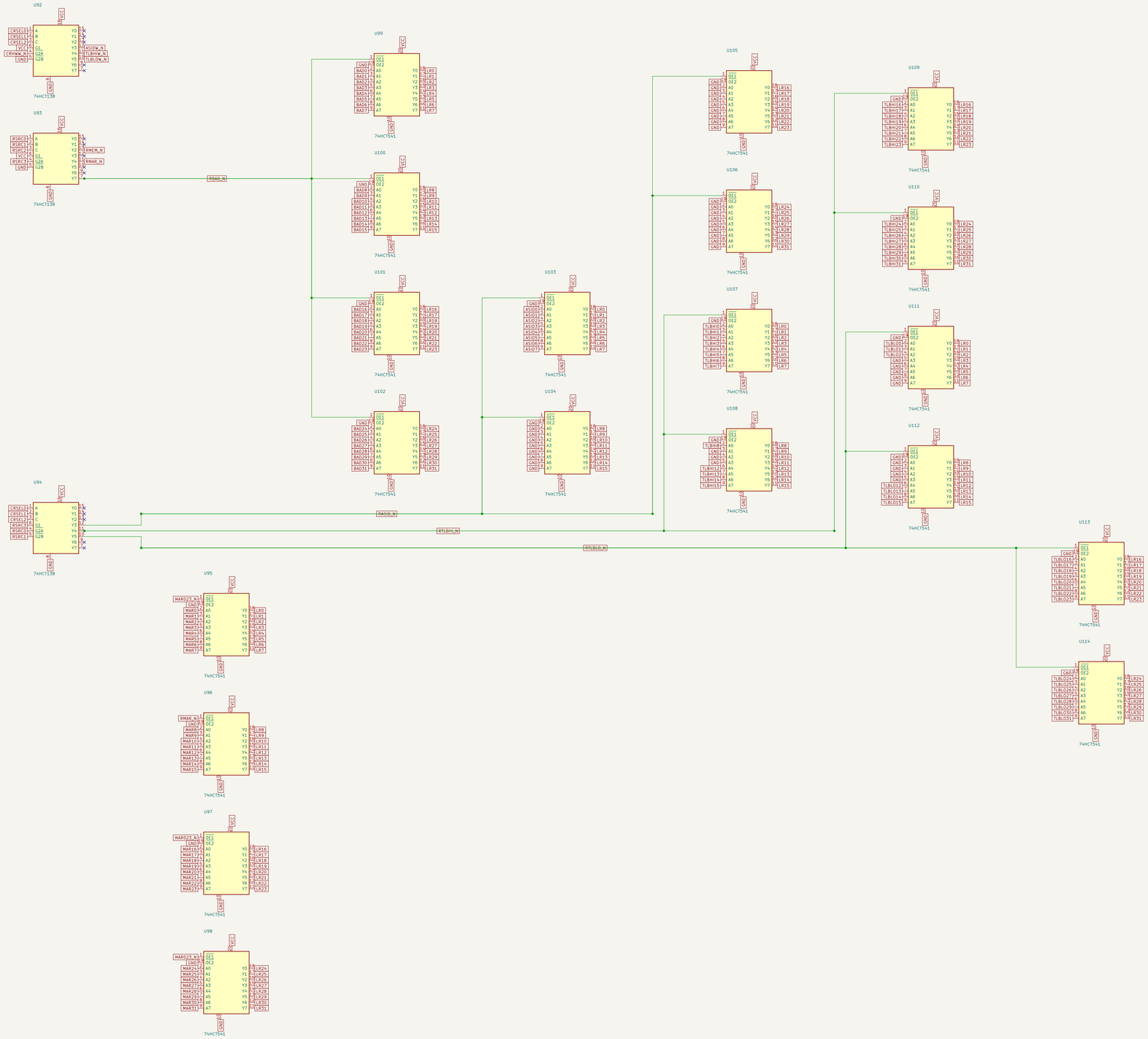
P32W

Sheet: /BADADDR/

File: mmu-card-9.kicad_sch

Title: P32W MMU / DATA CARD

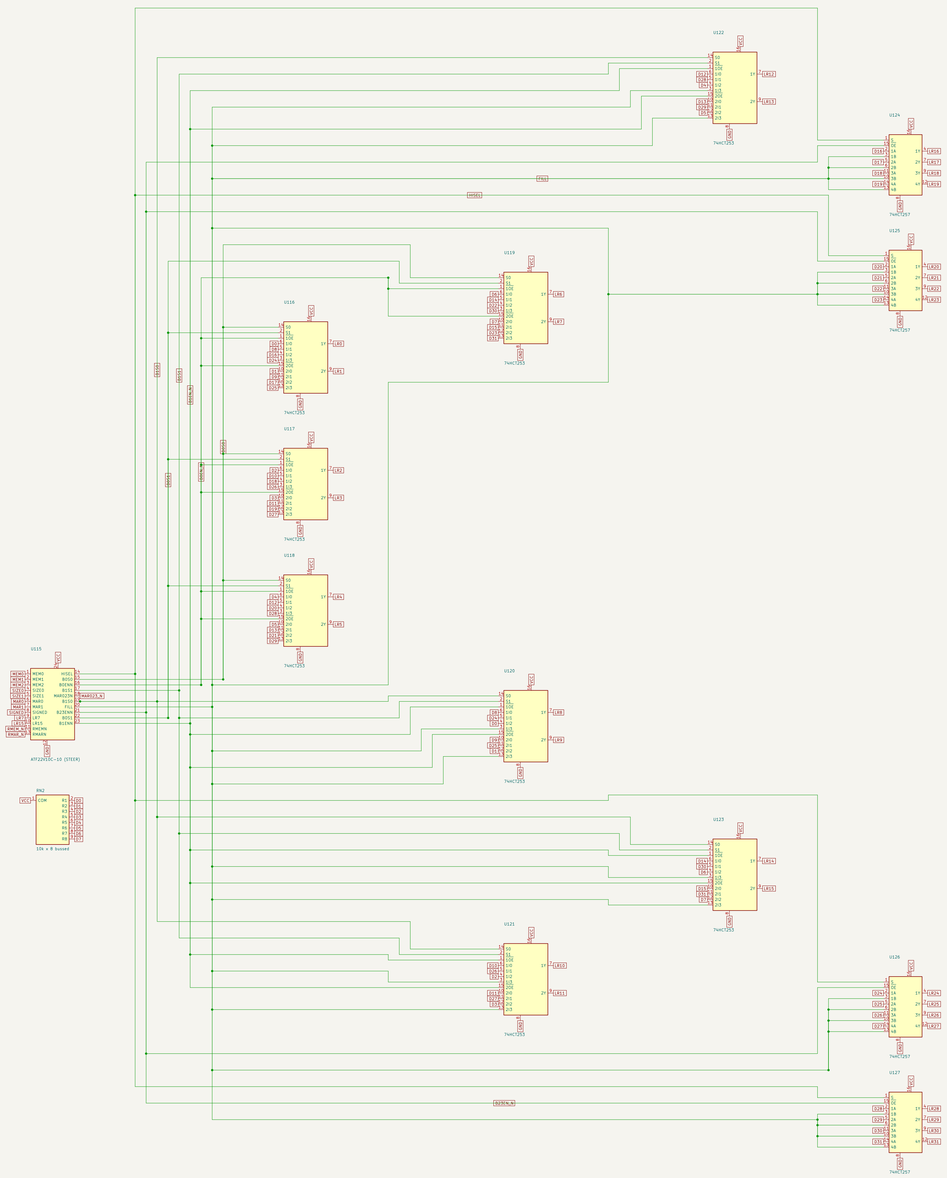
Size: A2	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 10/14



Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
Control registers onto R
P32W
Sheet: /Control registers onto R/
File: mmu-card-10.kicad_sch

Title: P32W MMU / DATA CARD

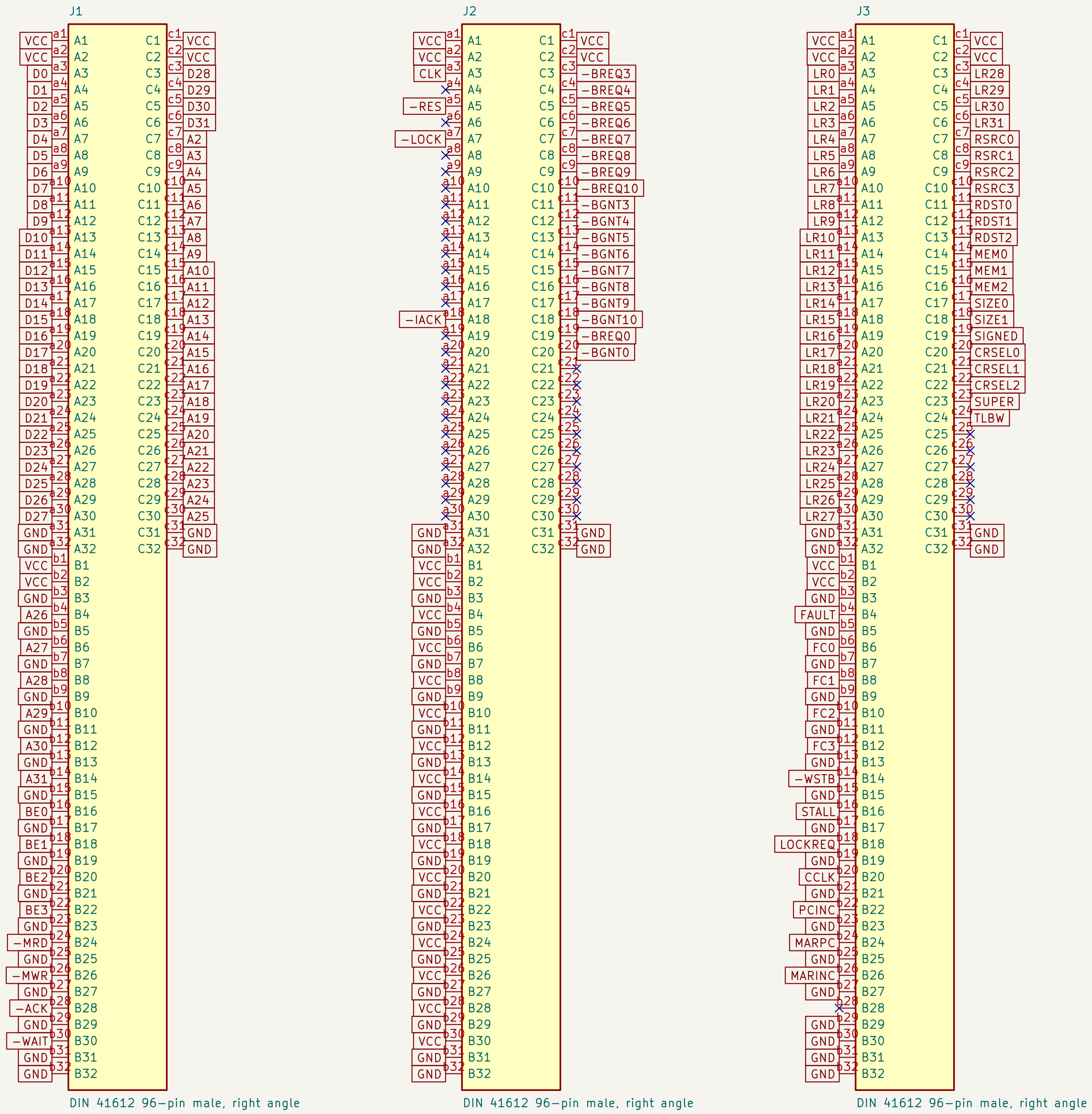
Size: A1	Date:	**Rev:**
KiCad E.D.A. 10.0.6		Id: 11/14



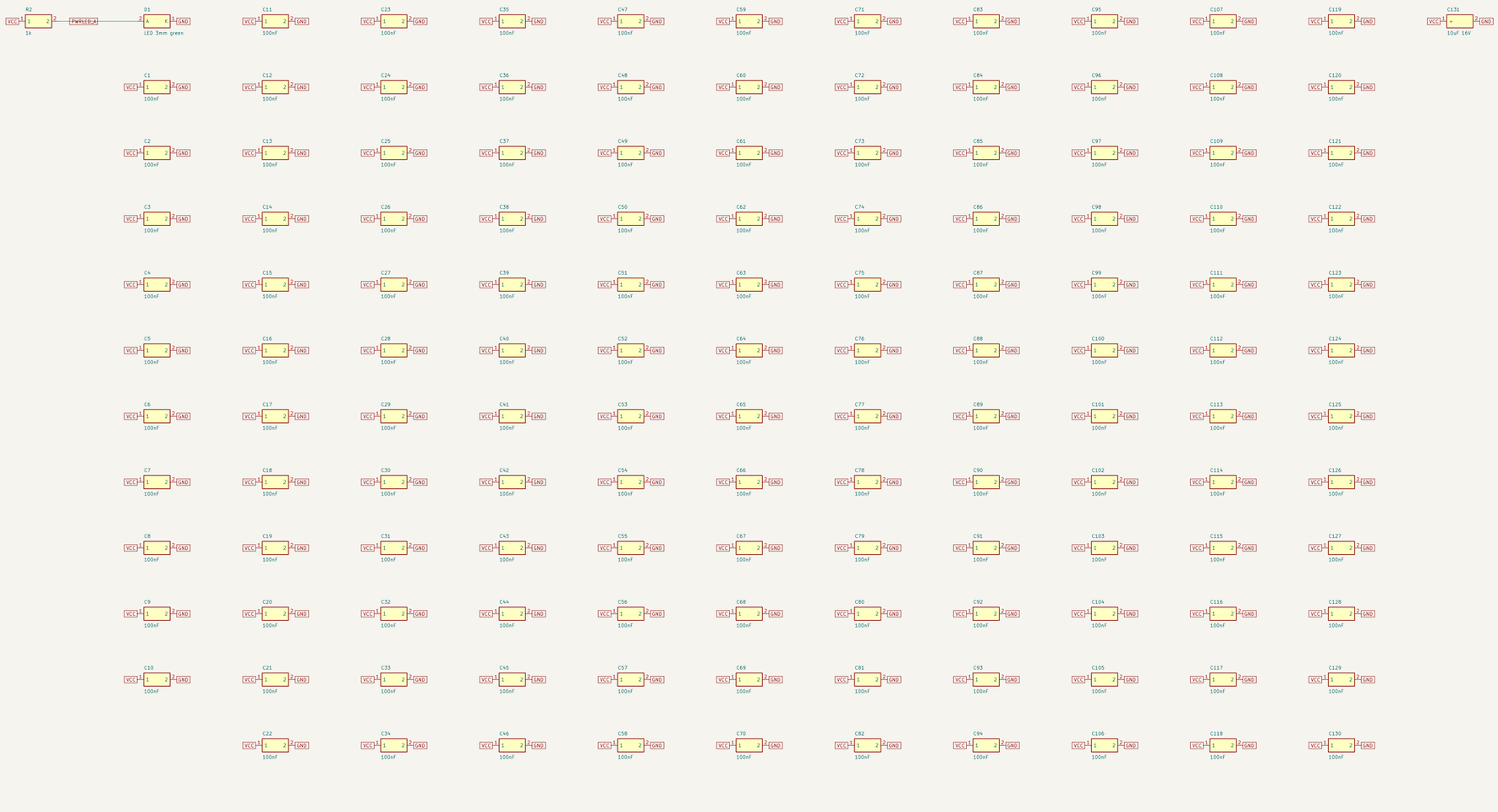
Generated by generators/p32klcad_sch.py from generators/card_mmu.py — do not edit
load steering
P32K
Sheet: /Load steering/
File: mmu-card-11.kicad_sch

Title: P32W MMU / DATA CARD

Size: A0	Date:	Rev:
KiCad 8.0.4 10.0.4		16 12/14



Generated by generators/p32kicad_sch.py from generators/card_mmu.py − do not edit
Backplane connectors
P32W
Sheet: /Backplane connectors/
File: mmu−card−12.kicad_sch
Title: P32W MMU / DATA CARD
Size: A3 | Date: | **Rev:**
KiCad E.D.A. 10.0.6 | Id: 13/14



Generated by generators/p32kicad_sch.py from generators/card_mmu.py — do not edit
Power: decoupling and bulk capacitors
P32W

Sheet: /Power: decoupling and bulk capacitors/	
File: mmu-card-13.kicad_sch	
Title: P32W MMU / DATA CARD	
Size: A1	Date:
KiCad E.D.A. 10.0.6	**Rev:** Id: 14/14